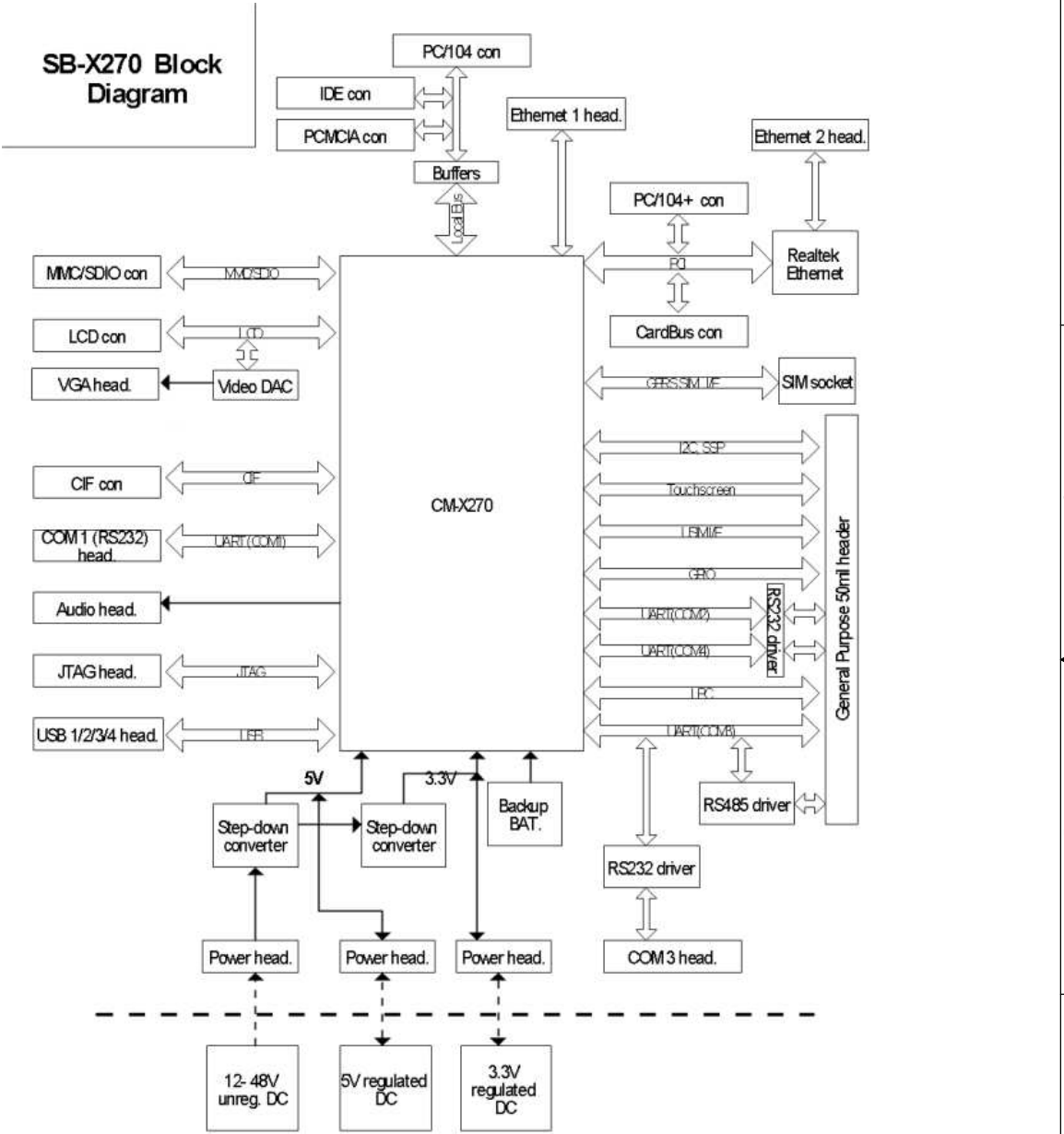


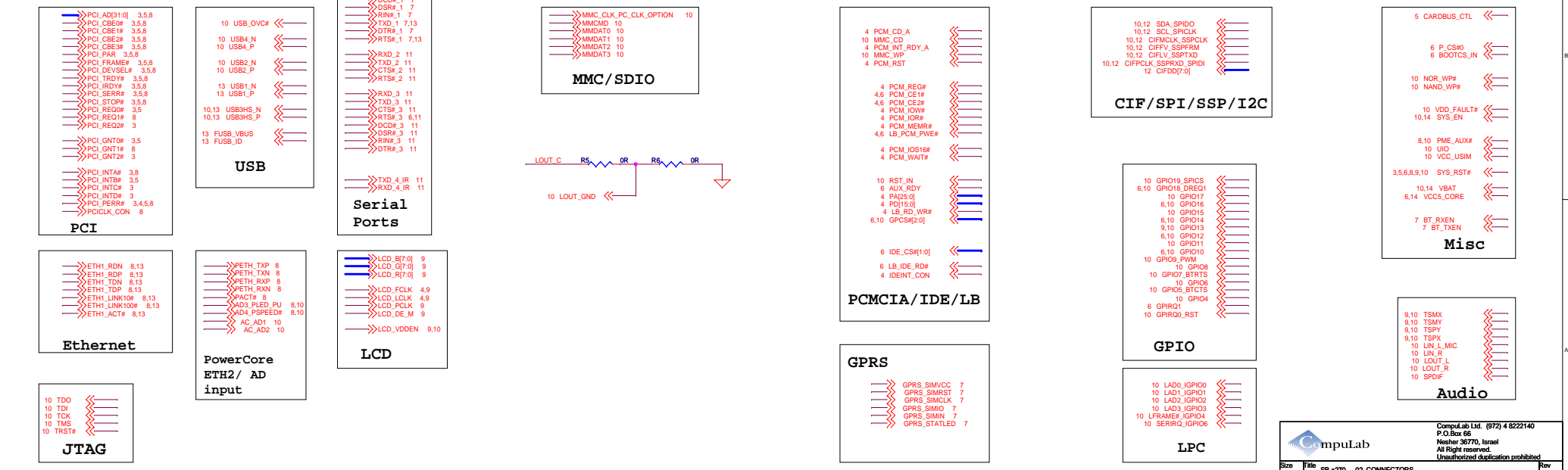
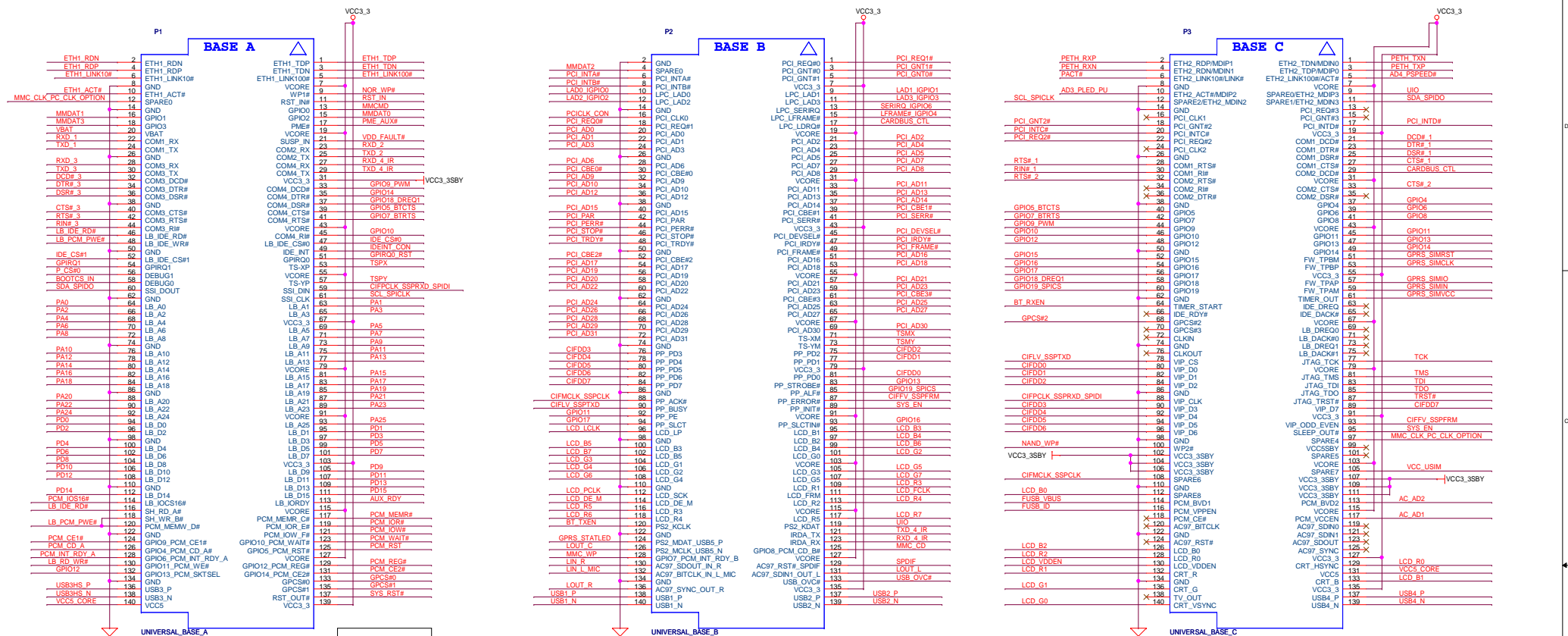
PAGE	FUNCTION
01	INDEX
02	CONNECTORS
03	PC104, PC104+ CONNECTORS
04	PCMCIA_LOGIC
05	CARDBUS
06	IDE_CPLD
07	PCCARD_MECHANICS
08	ETHERNET
09	CRT, LCD
10	HEADERS: USB, AUDIO, GPIO
11	SERIAL RS232/422/485 DRIVERS
12	CIF
13	FRONT_PANEL
14	POWER

# SB-x270

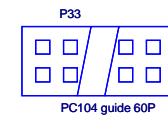
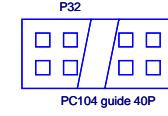
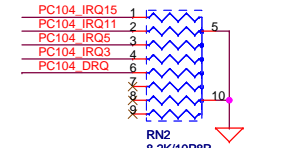
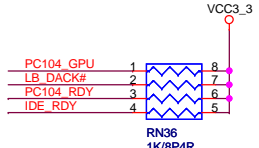
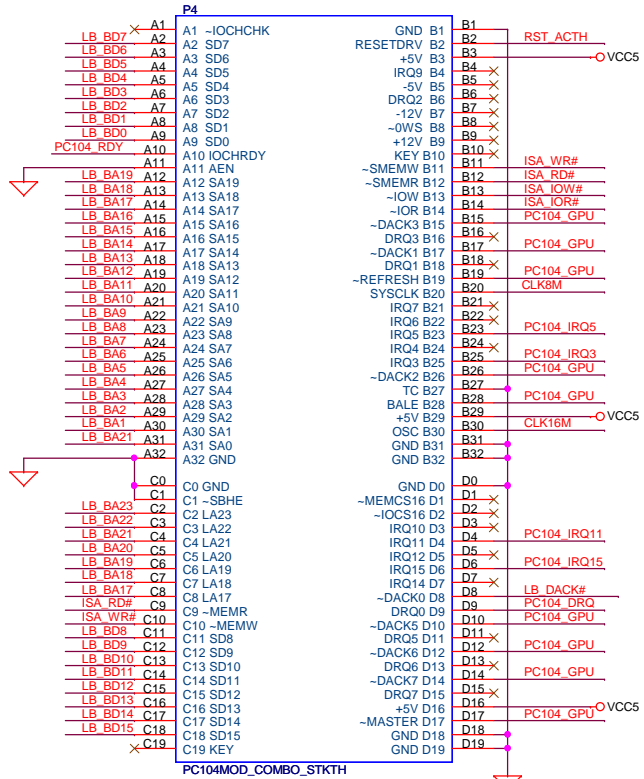
## Rev 1.3

ZZ1  
P/N = 188Z10030  
PCB1  
Rev 1.3  
PARSER\_VERSION\_1.0

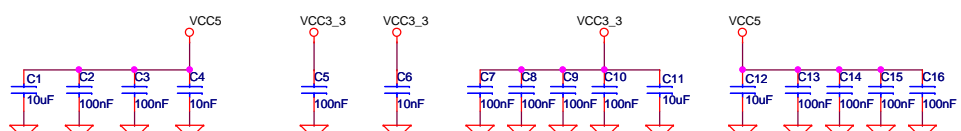
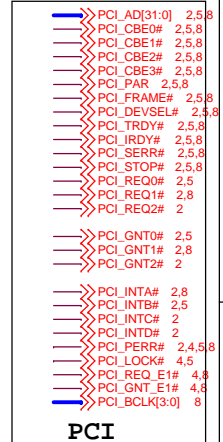
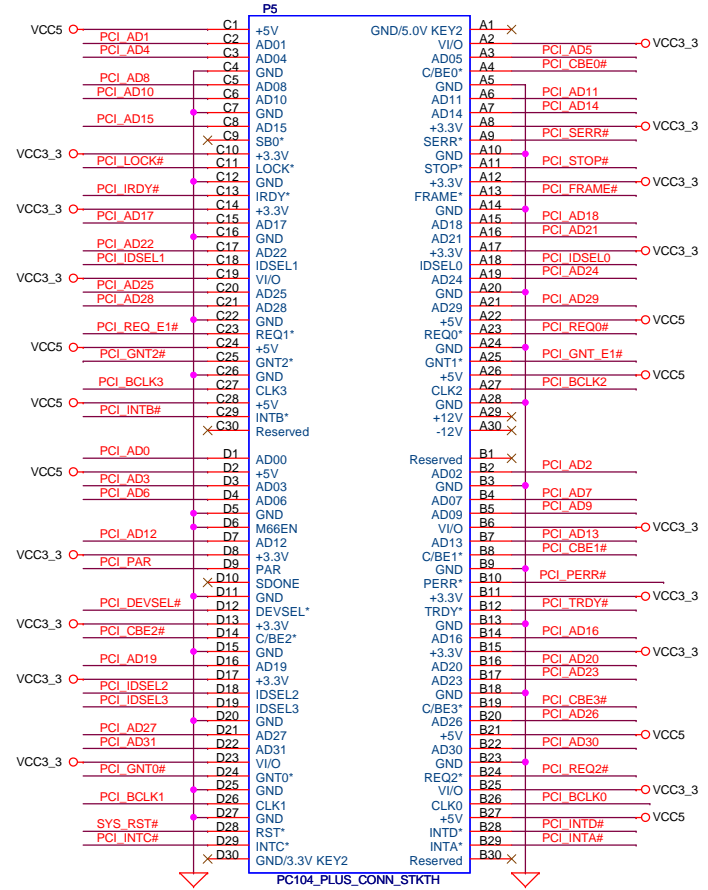




# ISA

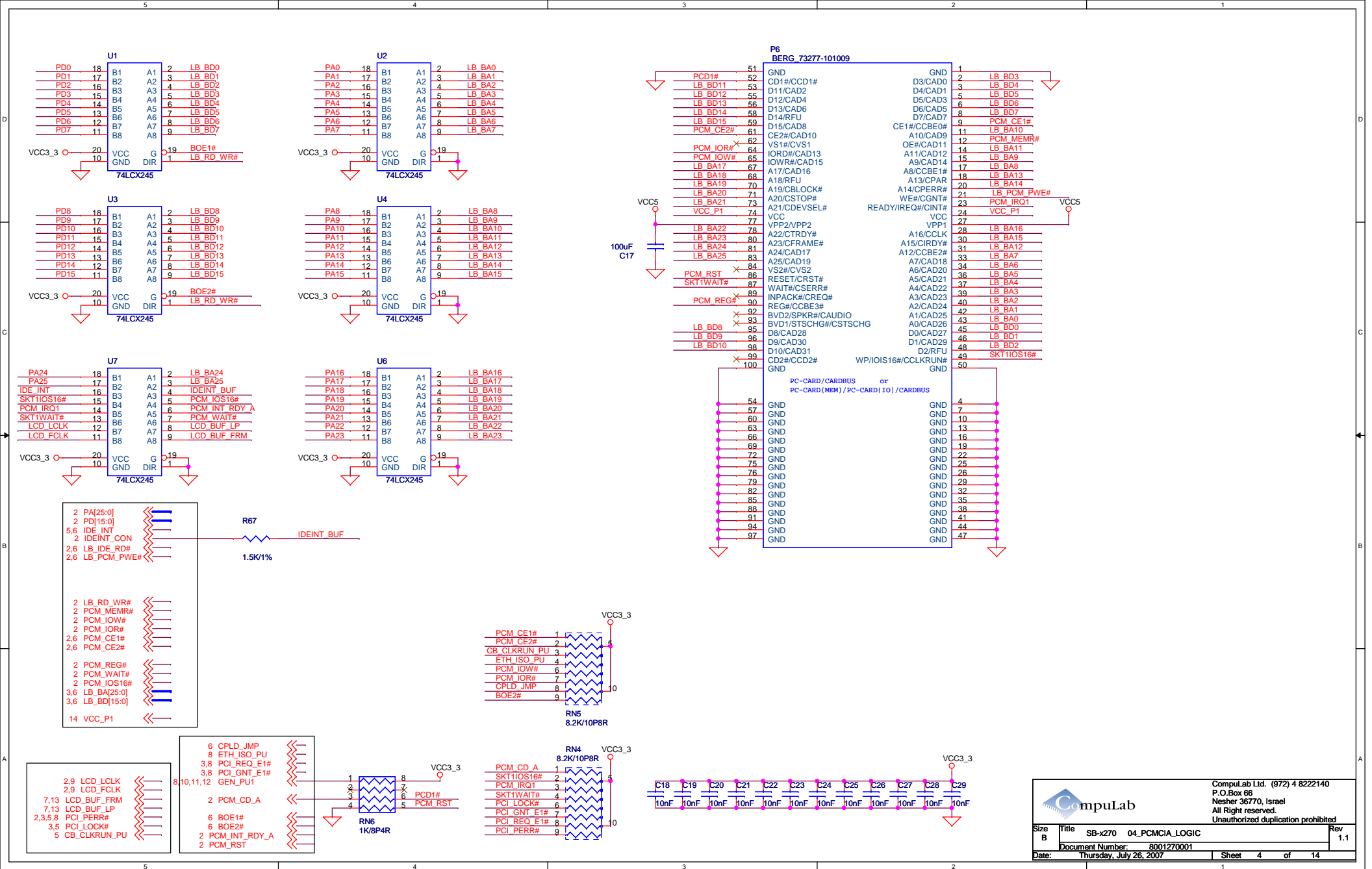


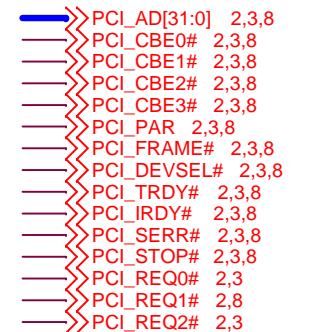
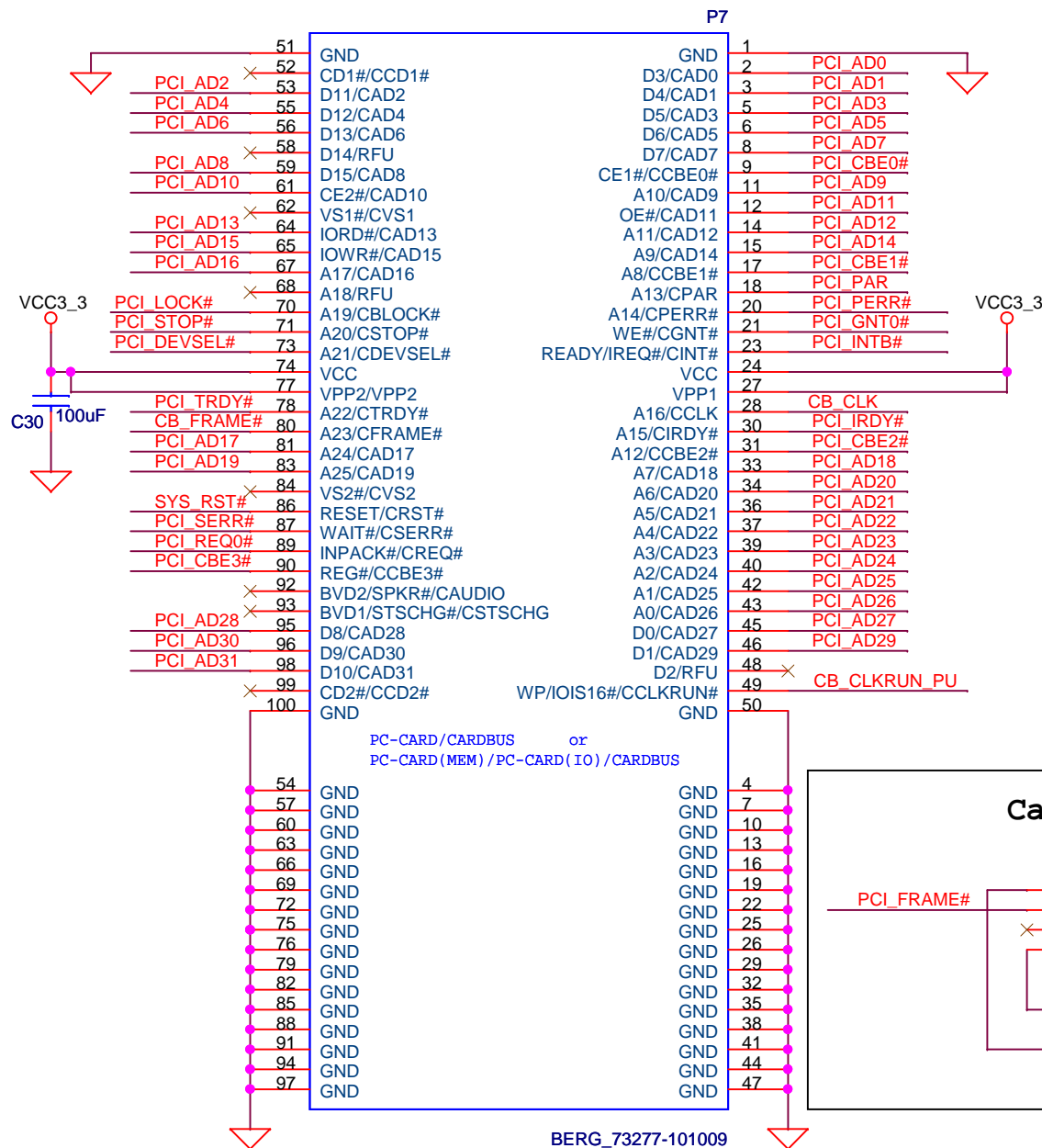
PCI



CompuLab Ltd. (972) 4 8222140  
P.O.Box 66  
Nesher 36770, Israel  
All Right reserved.  
Unauthorized duplication prohibited

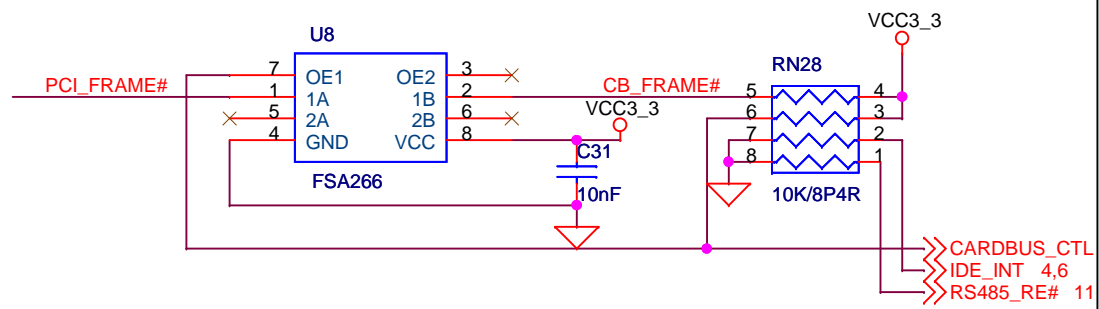
Size B	Title	SB-x270 03_PC104_CONN	Rev	1.1
	Document Number:	8001270001		
Date:	Thursday, July 26, 2007	Sheet	3	of 14





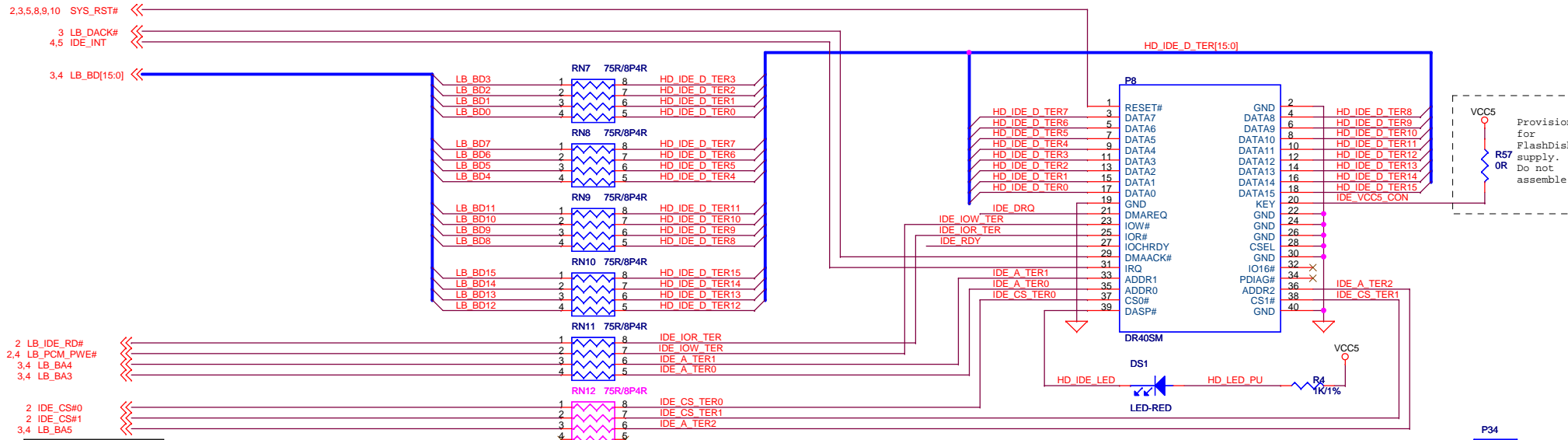
## PCI

## CardBus FRAME# block

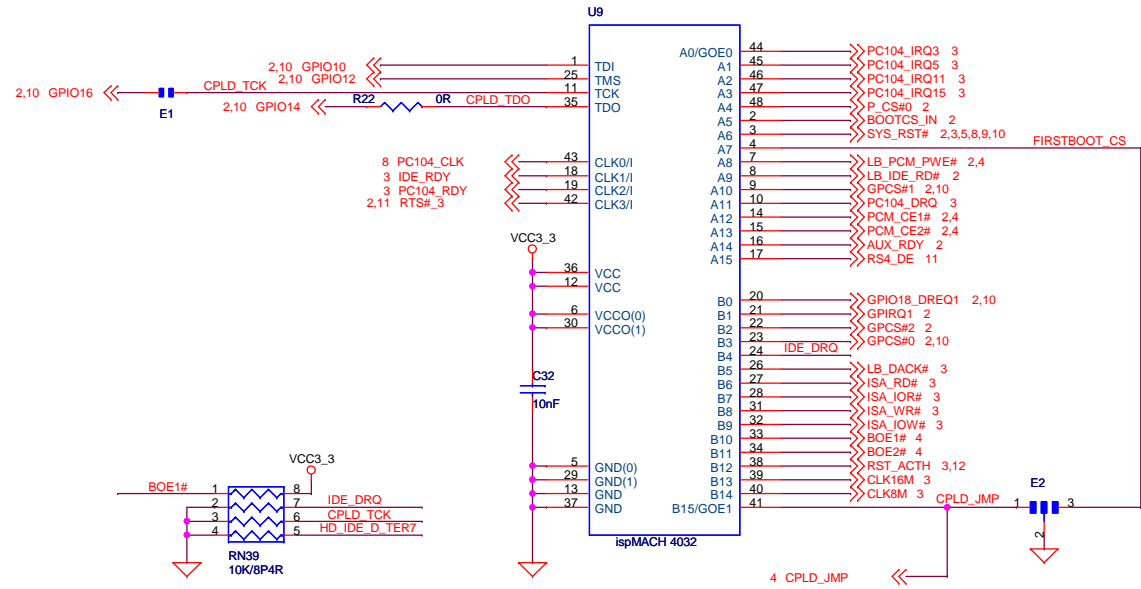
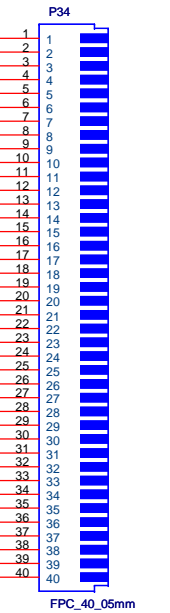


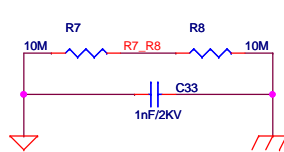
CompuLab Ltd. (972) 4 8222140  
P.O.Box 66  
Nesher 36770, Israel  
All Right reserved.  
Unauthorized duplication prohibited

Size	Title	Document Number:	Rev
A	SB-x270 05_CARDBUS	8001270001	1.1
Date:	Thursday, July 26, 2007	Sheet 5 of 14	

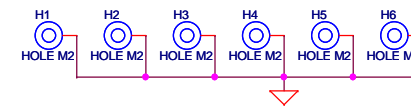


Provision for FlashDisk supply. Do not assemble.

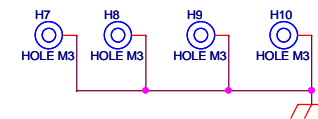




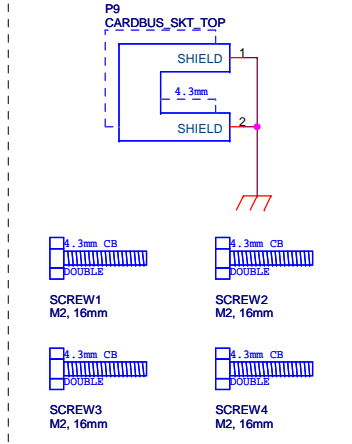
## CORE modules



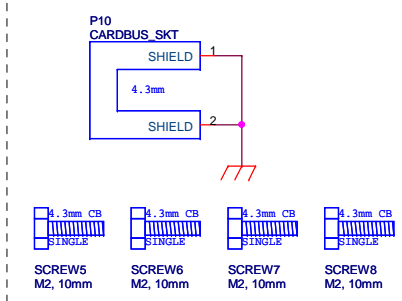
## BASE to enclosure



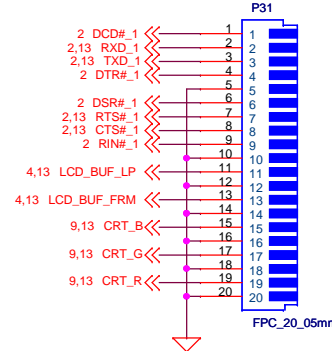
### DUAL SOCKET IMPLEMENTATION



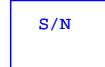
### SINGLE SOCKET IMPLEMENTATION



### FRONT PANEL REPLACEMENT



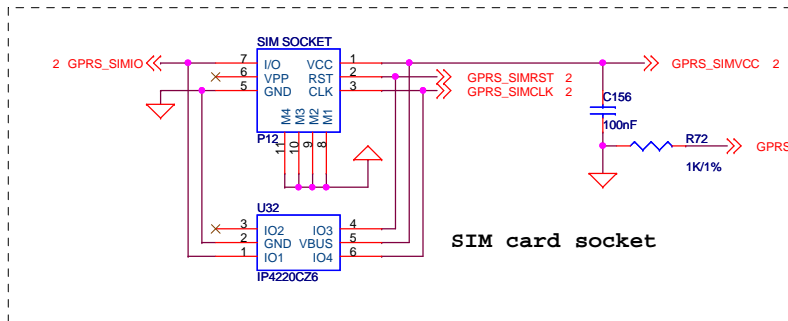
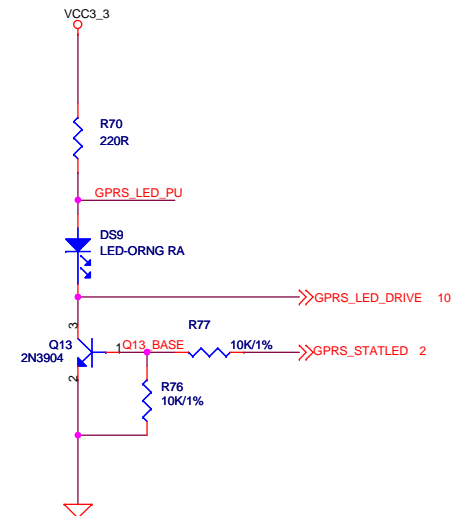
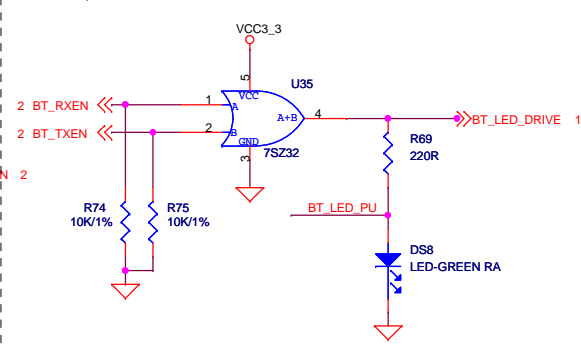
### LBL1 SERIAL NUMBER LABEL 0.8inx0.5in



### LBL2

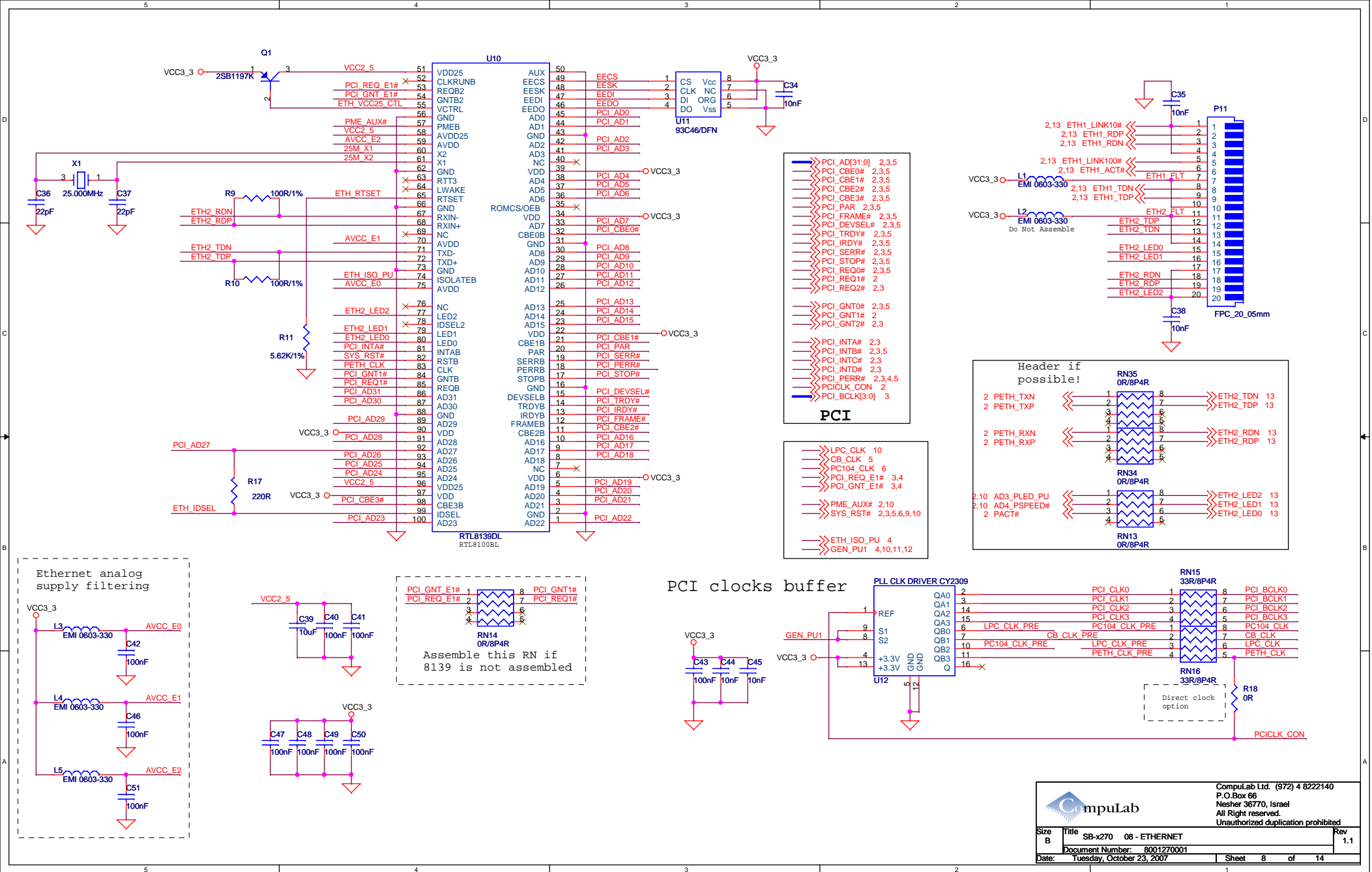


### BT/GPRS LEDs

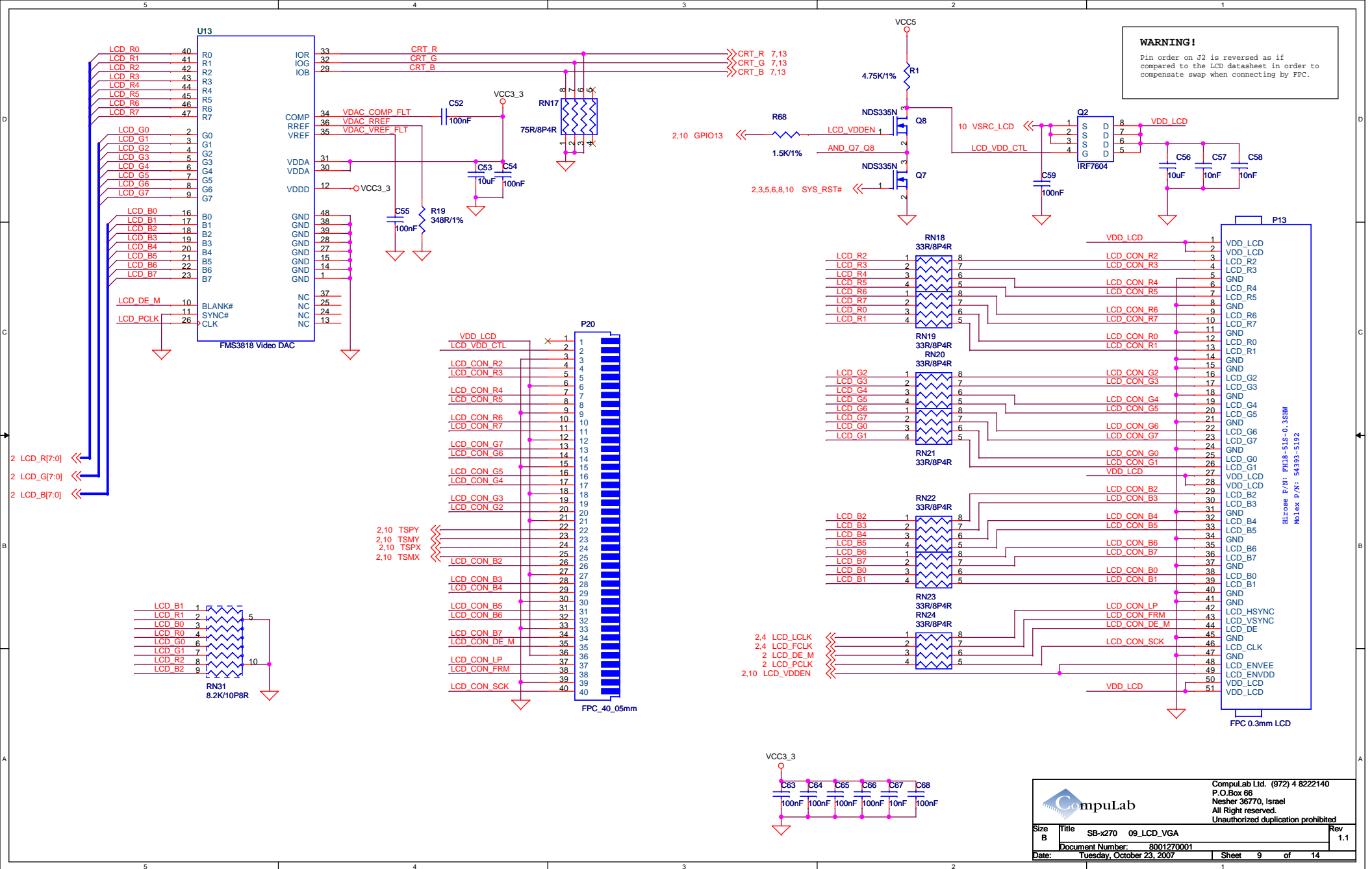


### SIM card socket








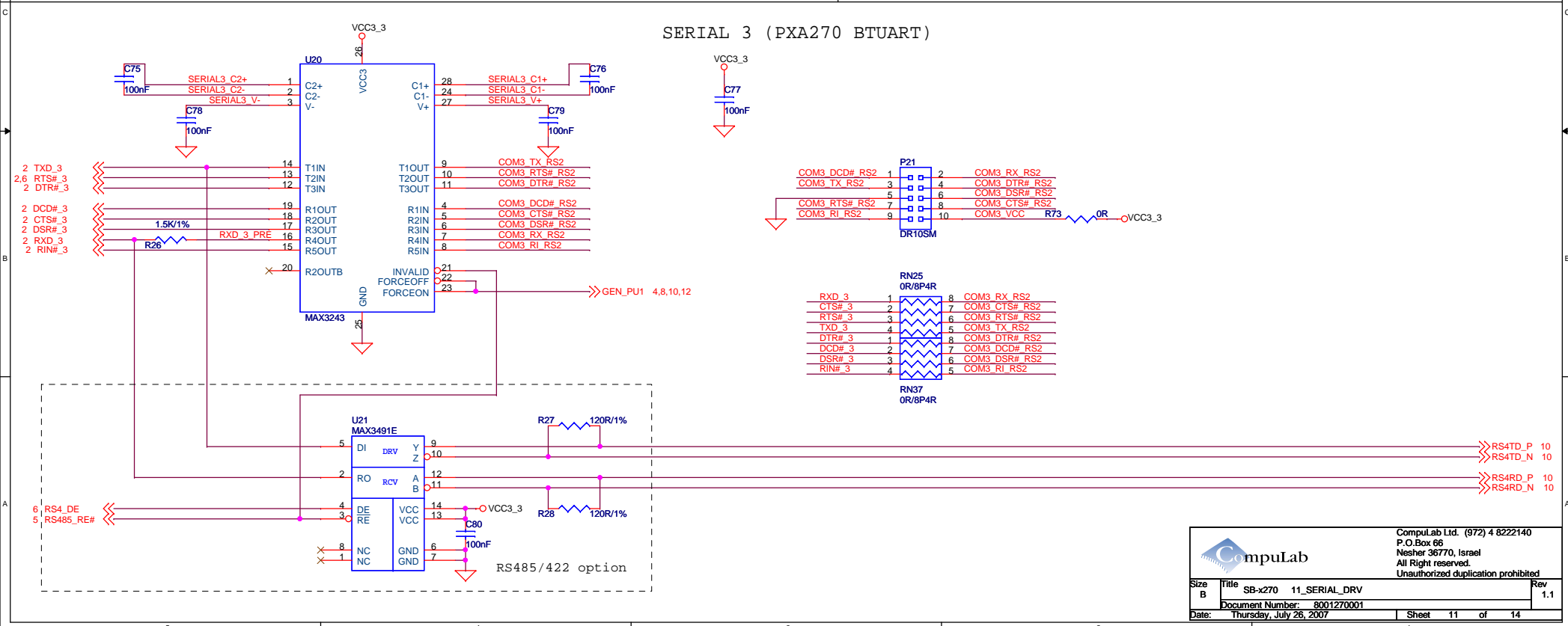
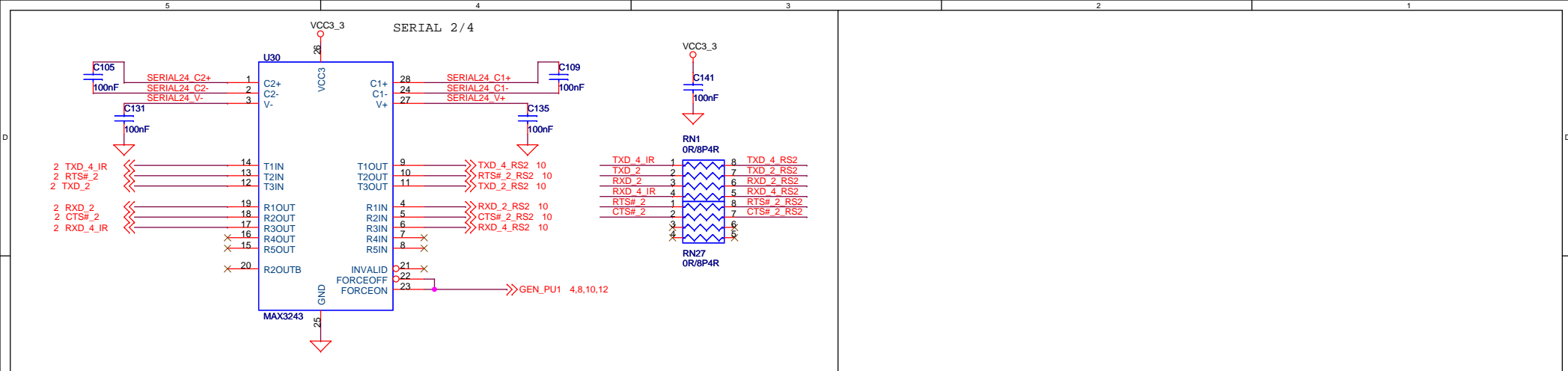


**WARNING!**  
Pin order on J2 is reversed as if compared to the LCD datasheet in order to compensate swap when connecting by FPC.

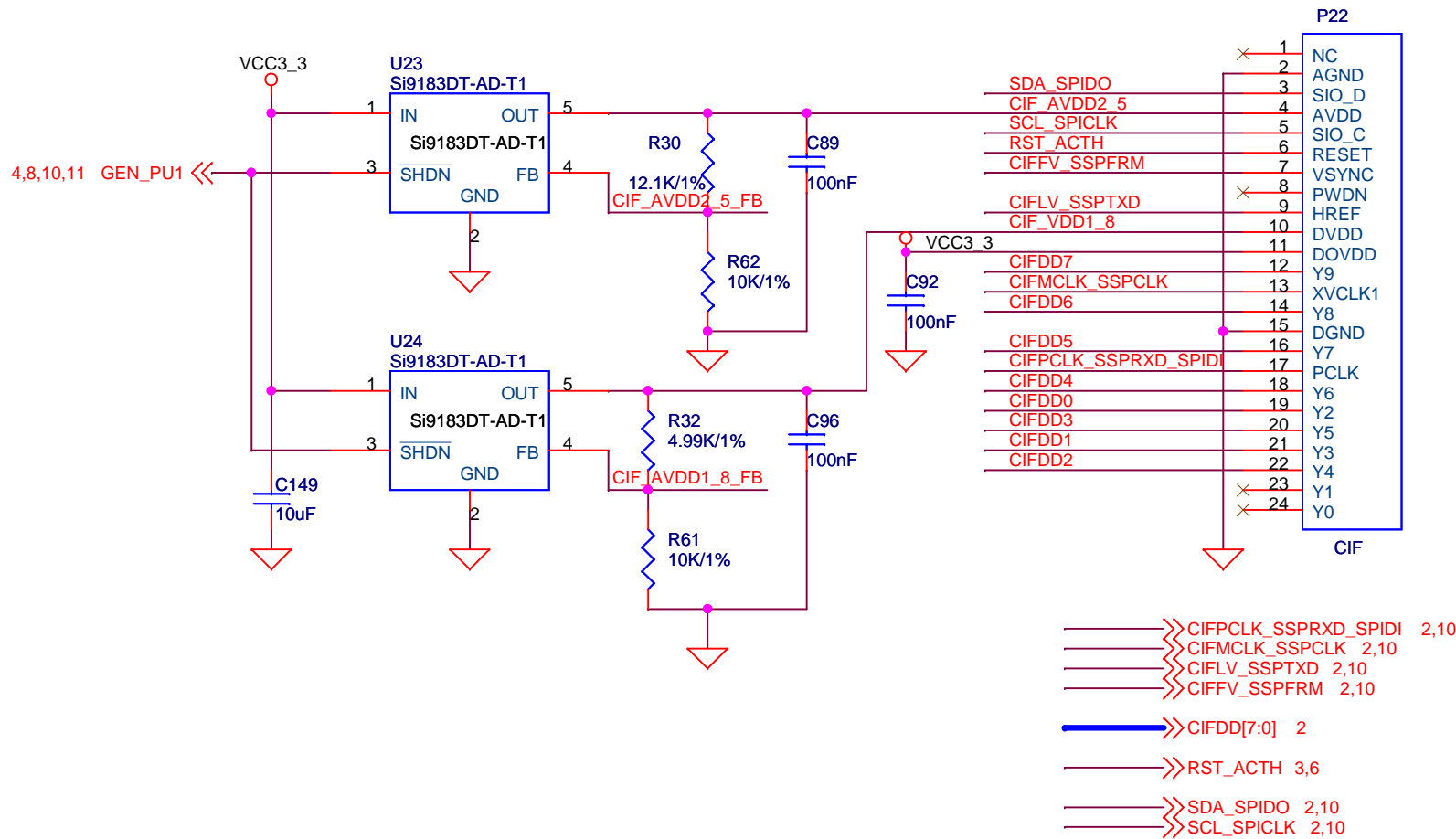
Hirom P/N: FB18-51S-0.35BW  
Molex P/N: 54393-5192

		CompuLab Ltd. (972) 4 8222140 P.O.Box 66 Nesher 36770, Israel All Right reserved. Unauthorized duplication prohibited	
Size B	Title SB-x270_09_LCD_VGA	Document Number: 8001270001	Rev 1.1
Date: Tuesday, October 23, 2007	Sheet 9	of 14	

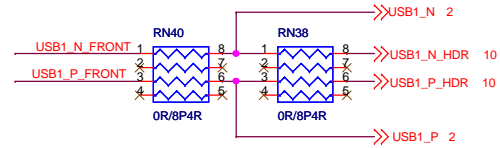
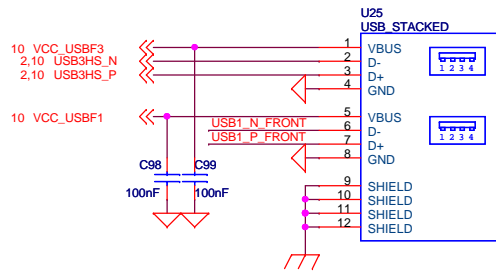




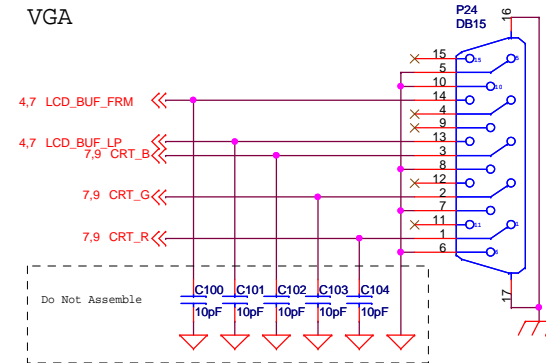
# CIF connector



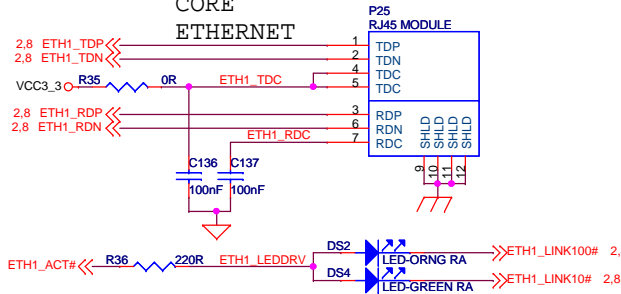
## USB



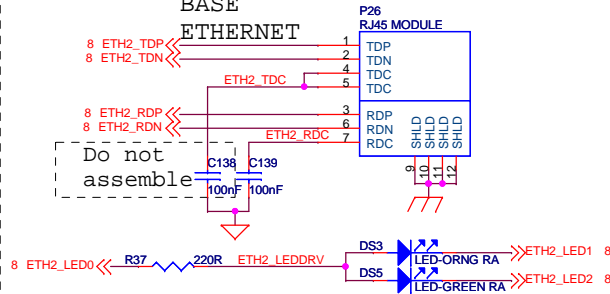
## VGA



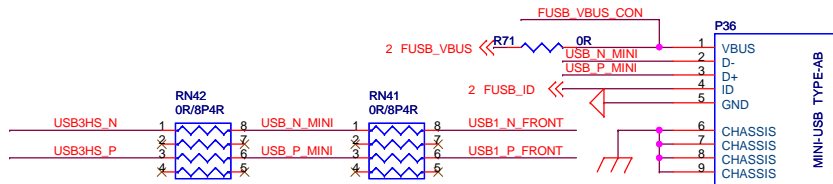
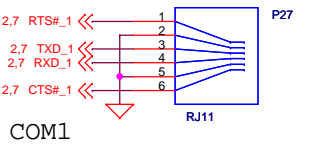
## CORE ETHERNET



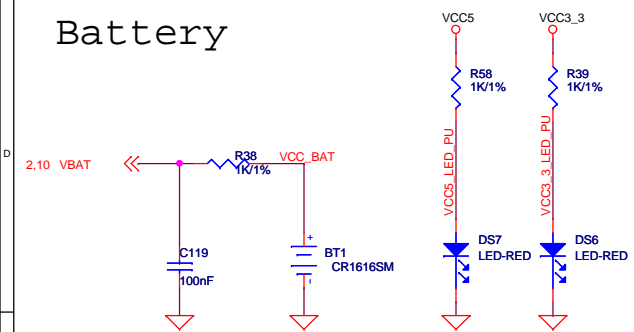
## BASE ETHERNET



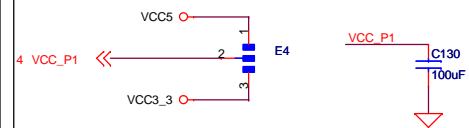
## COM1



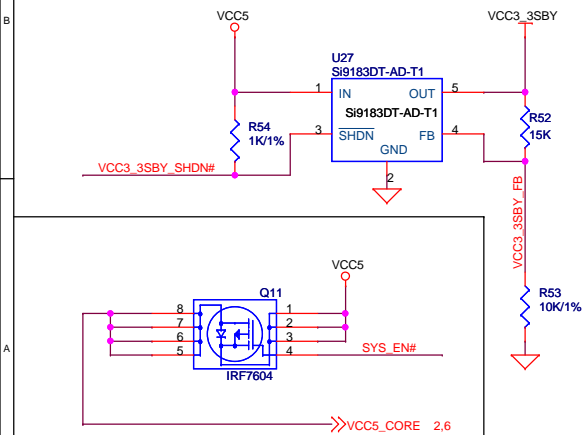
## Battery



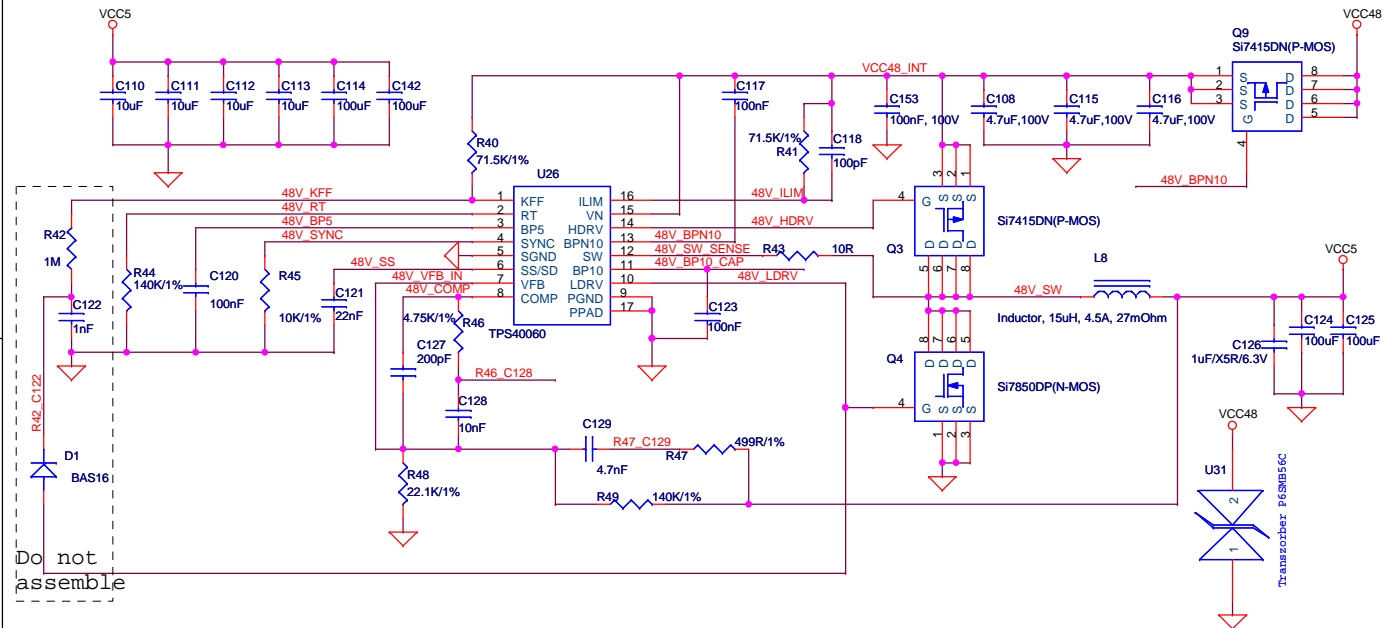
## PCMCIA POWER



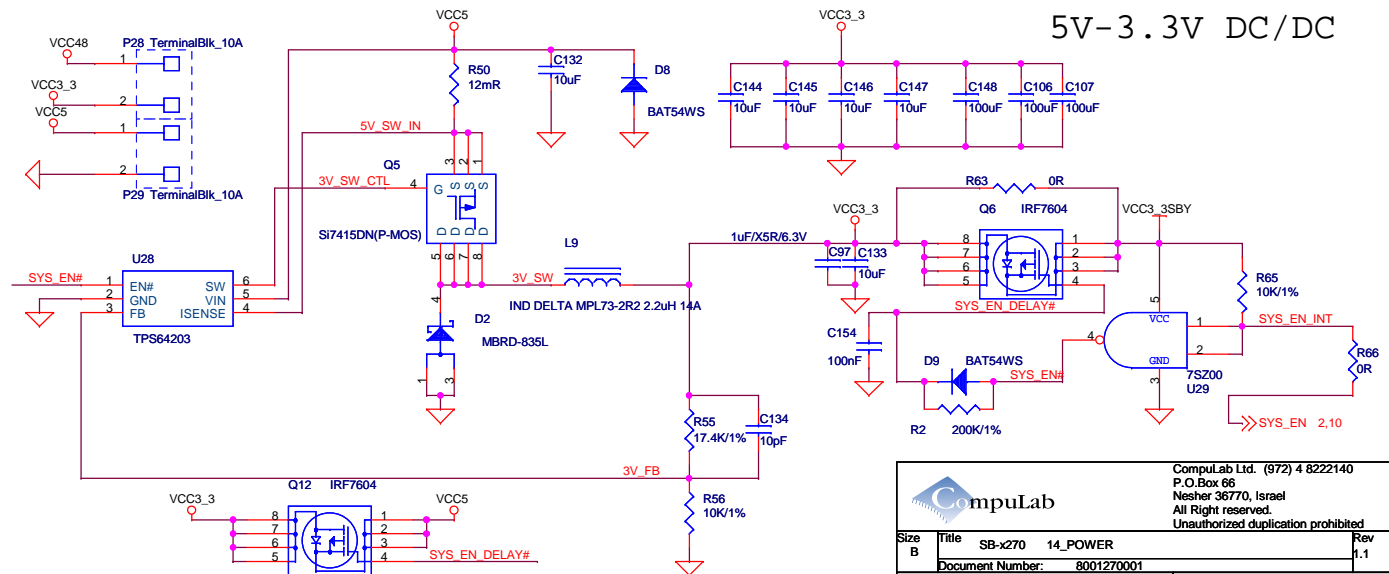
VCC Standby regulator



## 48V-5V DC/DC



5V-3.3V DC/DC



Rev 1.0	* Initial revision.
Rev 1.1	<ul style="list-style-type: none"><li>* U11 package changed to MO-229VCED-2 (DFN).</li><li>* Mechanical holes MCH9-12 connected to SHLD.</li><li>* Added RN1 &amp; RN27 (U30 bypass).</li><li>* Added pull-up on LCD_VDDEN.</li><li>* Routed UIO from CM-X270L.</li><li>* Added R3 to connect VCC_USIM to VCC3_3.</li><li>* Added D9,R2,C154 to delay Q6 turn on.</li><li>* Disconnected SYS_EN from peripherals.</li><li>* SDIO socket (P30) pin 15 connected to GND.</li><li>* Added Q6 bypass (R63).</li><li>* Added Q10 for SDIO power control.</li><li>* C117 &amp; C123 package changed to RC0603.</li></ul>
Rev 1.2	<ul style="list-style-type: none"><li>* Added Q11 to cut off 5V supply from the CORE and HD during sleep.</li><li>* Added R67 to enable usage of PXA-270 GPIO[100] .</li><li>* Packages of P11 &amp; P31 changed to FPC05SM20-1.</li><li>* Removed pull-up on PME_AUX#.</li><li>* Removed R23.</li><li>* Added 40 pin LCD connector (P20).</li><li>* Added Added R68 to connect GPIO13 to LCD_VDDEN.</li><li>* Added Q12 to simplify 3.3V only operation.</li><li>* Changed Changed P19 to 2x7 100mil.</li><li>* Connected RST_IN and VCC3_3SBY to P19.</li><li>* Switched to 100uF ceramics.</li><li>* Added R74,R75 in order to cut USB1 port routing from front panel on boards without F1 option.</li><li>* Changed C93, C94 to 33pF. Removed C95, L7 from assembly.</li><li>* Added SIM card socket.</li><li>* Added miniUSB socket.</li><li>* Added GPRS and BT LEDs.</li><li>* Added 40 pin IDE connector (P34).</li><li>* Added SDIO ESD protection (U33, U34).</li></ul>
Rev 1.3	<ul style="list-style-type: none"><li>* Routed BT and GPRS LED drivers to P16.</li><li>* Added a transistor to GPRS LED driver.</li><li>* Provided footprints for CAMI and PC104 holes.</li><li>* Provided for separate audio ground.</li><li>* Changed L9 to MPL73.</li><li>* Fixed LCD routing to P20.</li></ul>