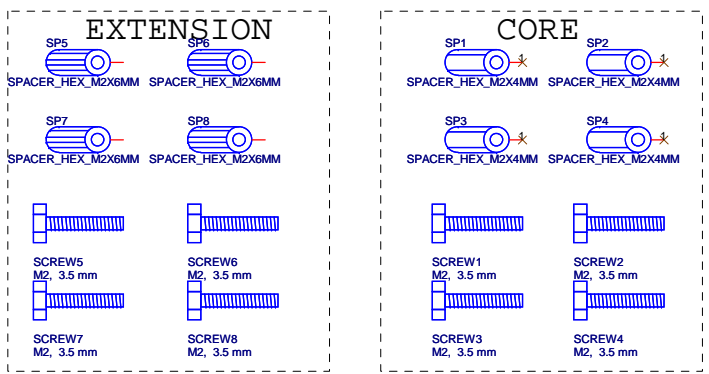
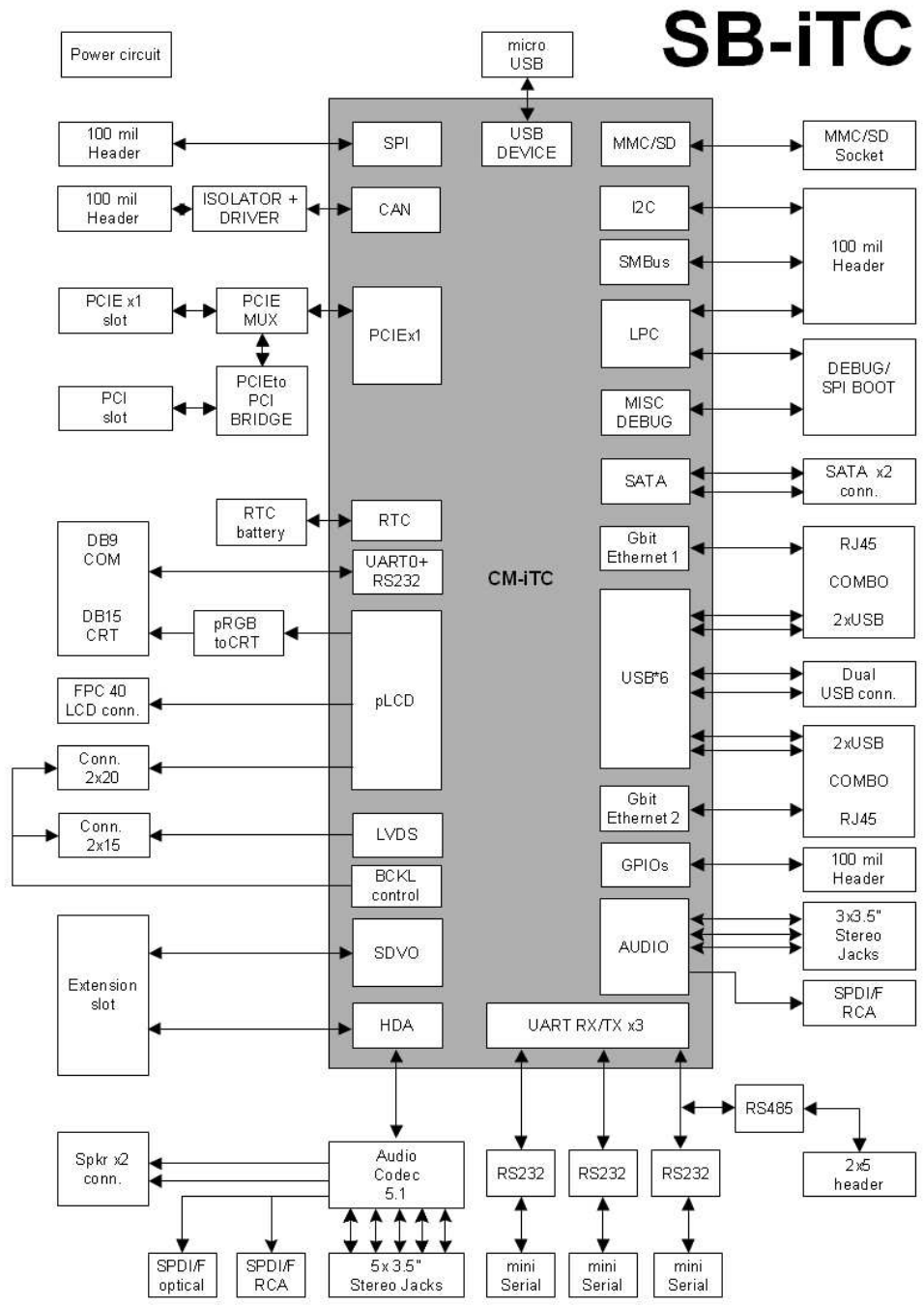
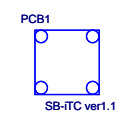
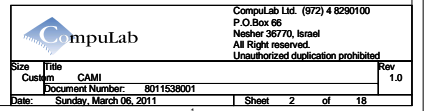


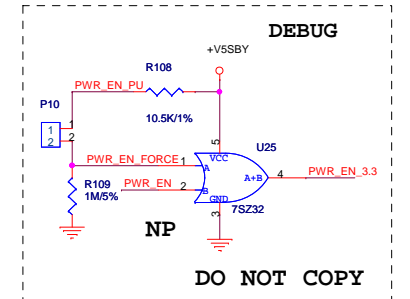
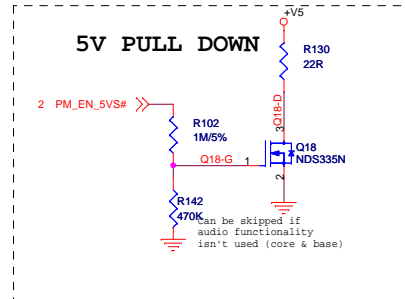
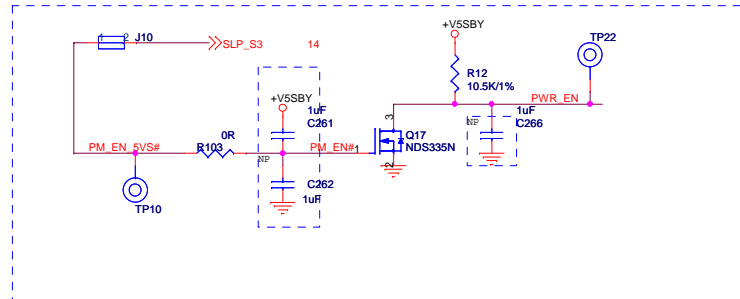
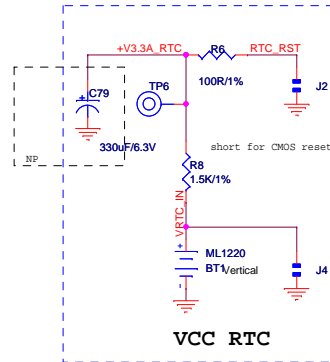
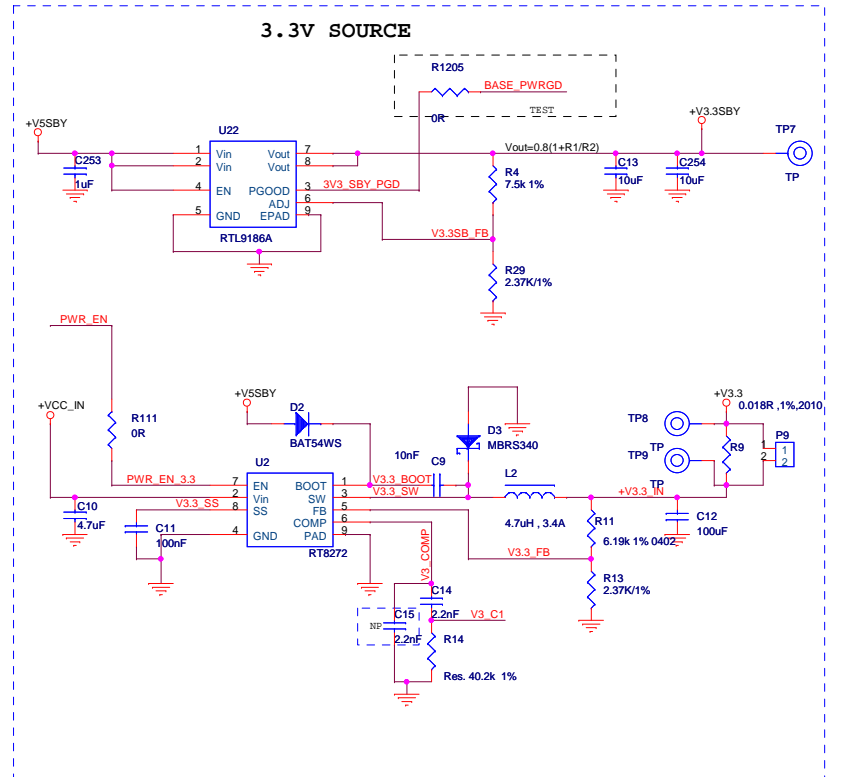
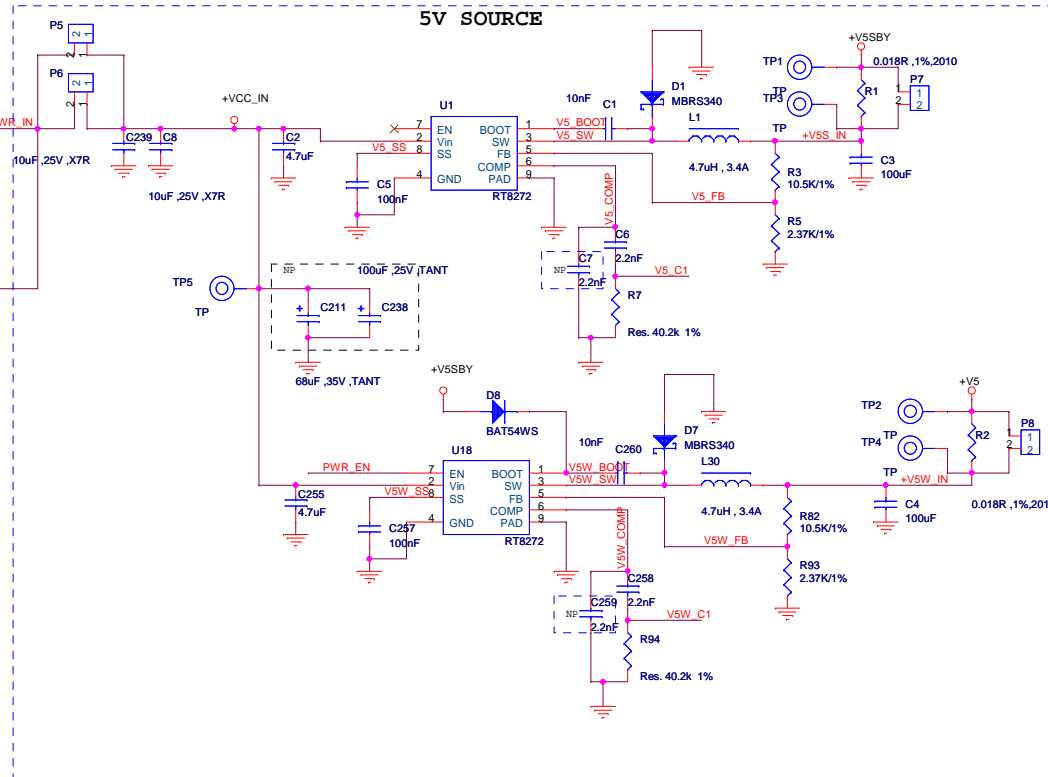
PAGE	FUNCTION	Rev 1.1
01	INDEX	
02	CAMI CONNECTORS	
03	POWER	
04	ETHERNET & USB	
05	AUDIO	
06	SDIO & SATA & CAN	
07	LCD & LVDS	
08	CRT & COM	
09	SDVO slot	
10	PCIE MUX & PCIE connector	
11	PCIE to PCI bridge	
12	PCI slot	
13	COM RS232_485	
14	MISC	
15	PORT80	
16	DEBUG	
17	PCIE to USB	
18	PCIE to USB (PWR)	



ZZ1
P/N = 188Z10030
PARSER_VERSION_1.0









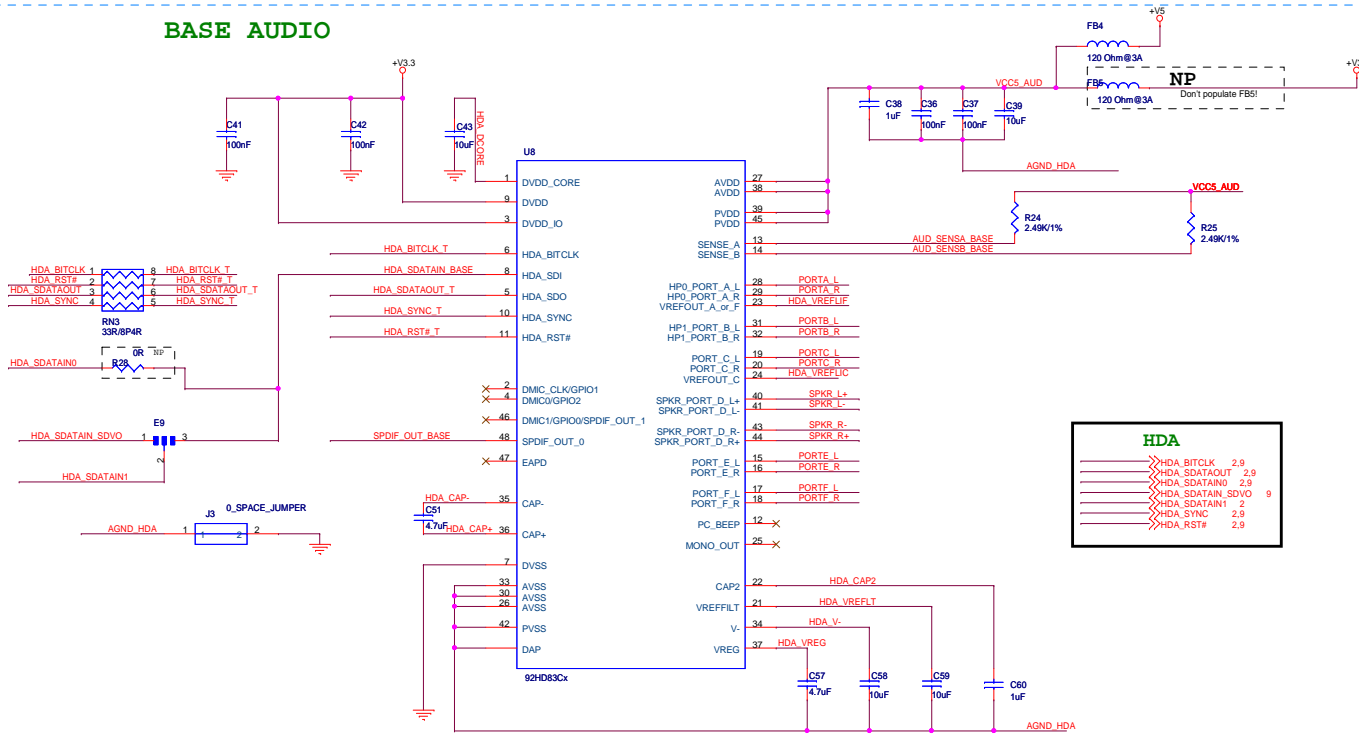
NOTE:
Schematic shows rechargeable ML1220 battery for the RTC circuit
External diode should be placed between R8 and R6 when using non-rechargeable battery to prevent charging

MISC

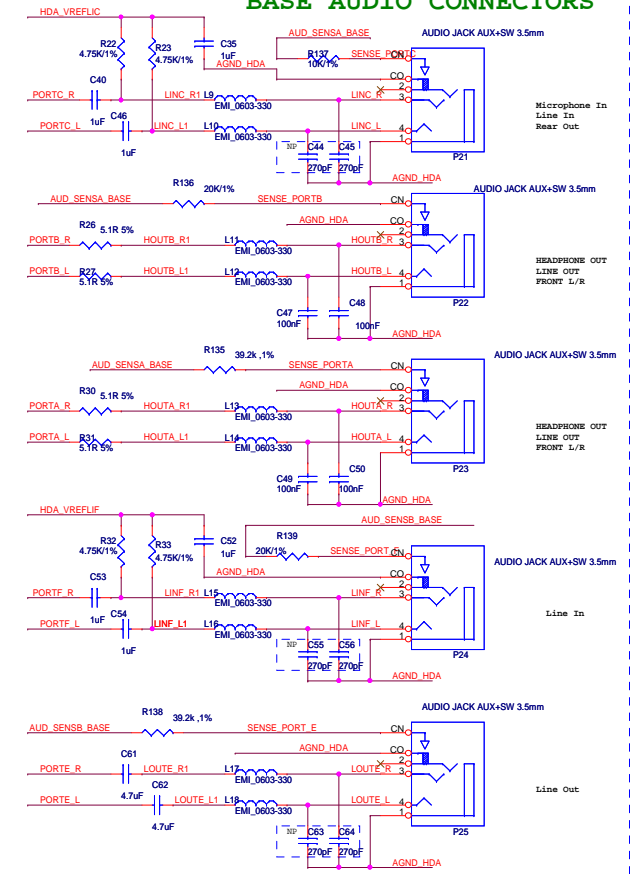
 2
 2,14

 <div style="display: inline-block; vertical-align: middle; margin-left: 10px;"> <p>CompuLab Ltd. (972) 4 8290100 P.O.Box 66 Nesher 36770, Israel All Right reserved. Unauthorized duplication prohibited</p> </div>		<p>Rev 1.0</p>
Size	Title POWER	
<p>Customer Document Number: 8011538001</p>		
Date:	Wednesday, October 19, 2011	<p>Sheet 3 of 18</p>

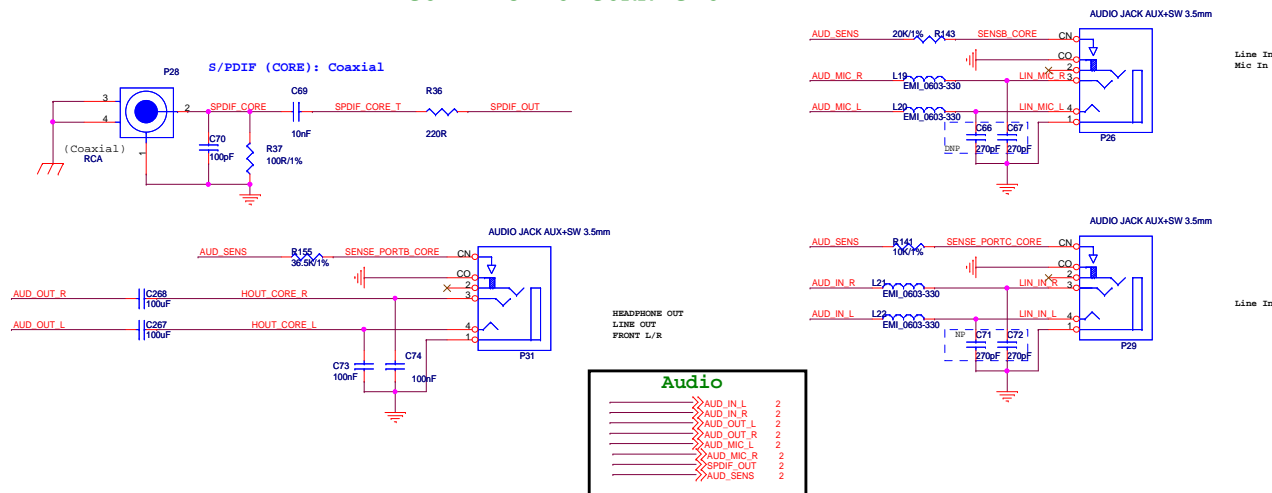
BASE AUDIO



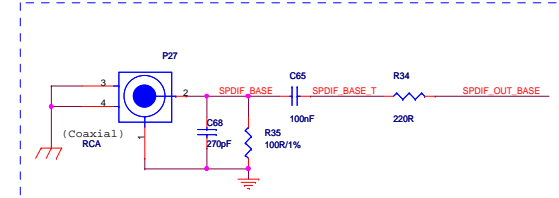
BASE AUDIO CONNECTORS



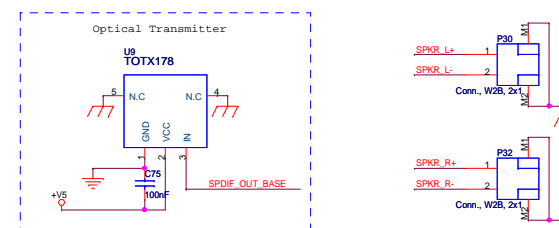
CORE AUDIO CONNECTORS



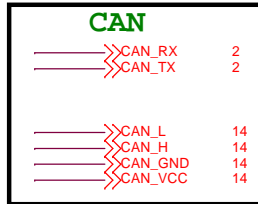
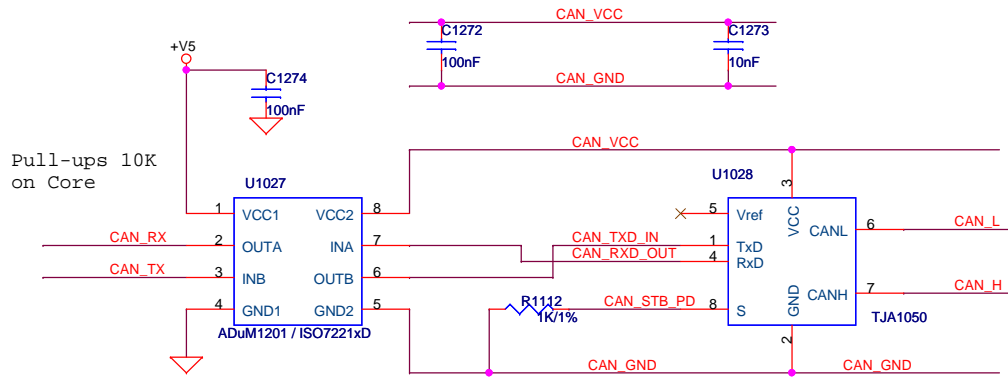
S/PDIF module option 2(BASE): Coaxial



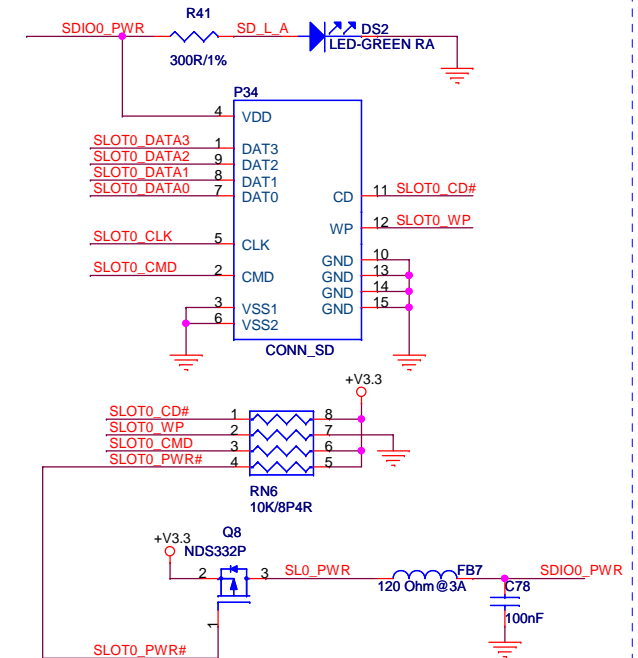
S/PDIF module option 1(BASE): Optical



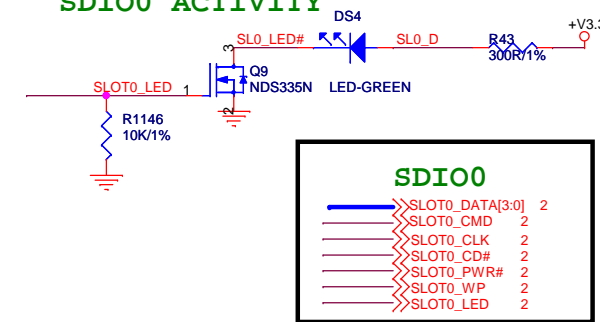
CAN INTERFACE + ISOLATOR + TRANSCEIVER



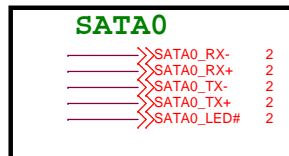
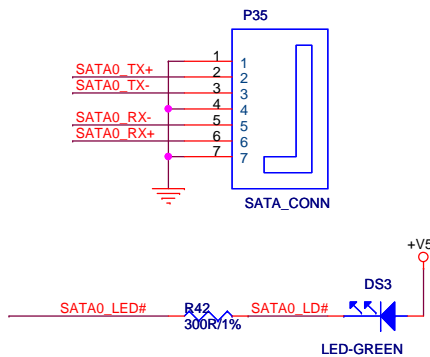
SDIO SOCKET 0



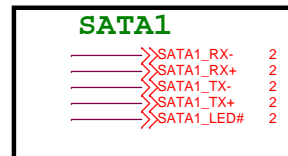
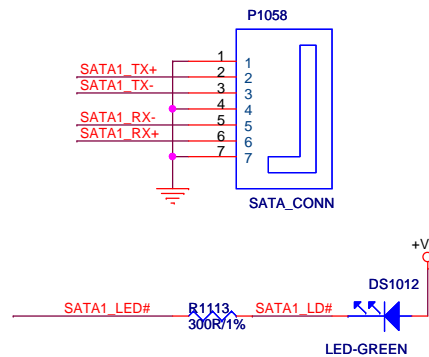
SDIO0 ACTIVITY



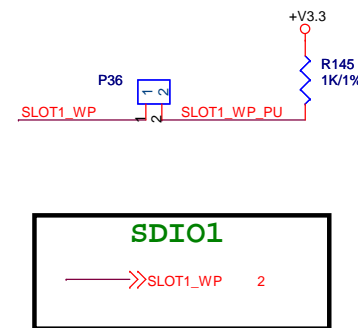
SATA0 CONNECTOR



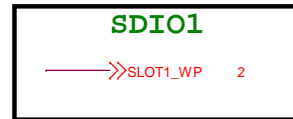
SATA1 CONNECTOR



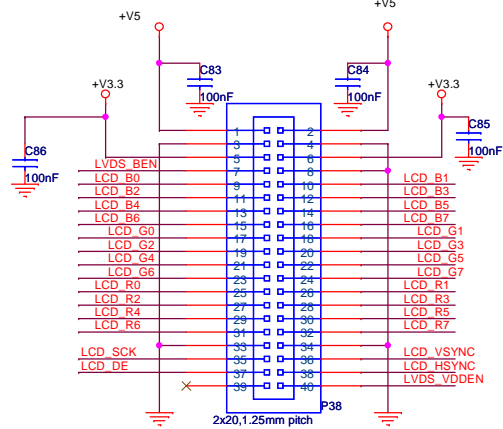
SDIO1- WP



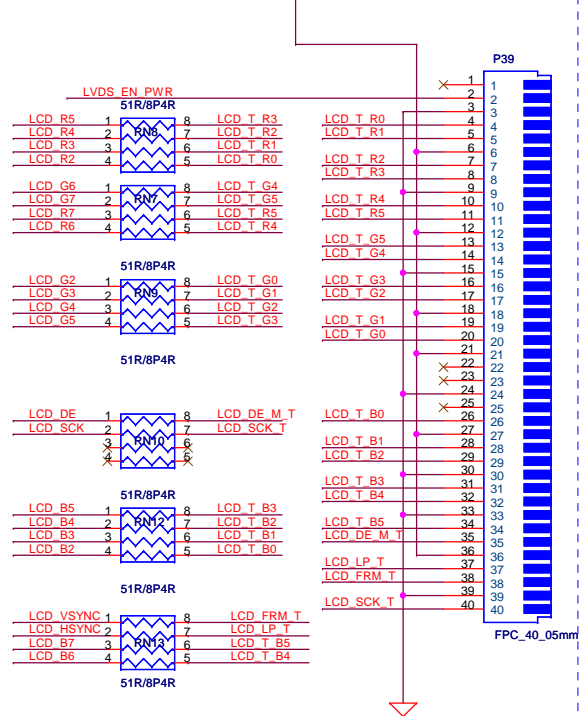
SDIO1



24 bit LCD connector



18 bit LCD connector



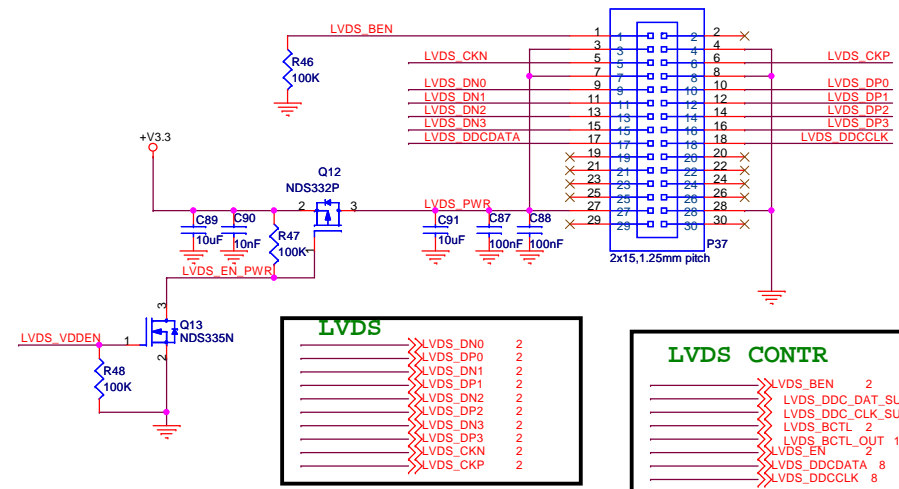
LCD

LVDS_VSYNC	2,8
LVDS_HSYNC	2,8
LVDS_SCK	2,8
LVDS_DE	2,8
LVDS_B[7:0]	2,8
LVDS_G[7:0]	2,8
LVDS_R[7:0]	2,8

MISC

STRAP_EN	2
GEN_PU1	4

LVDS

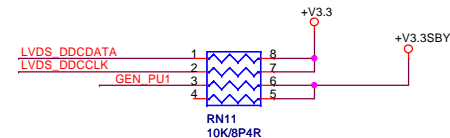
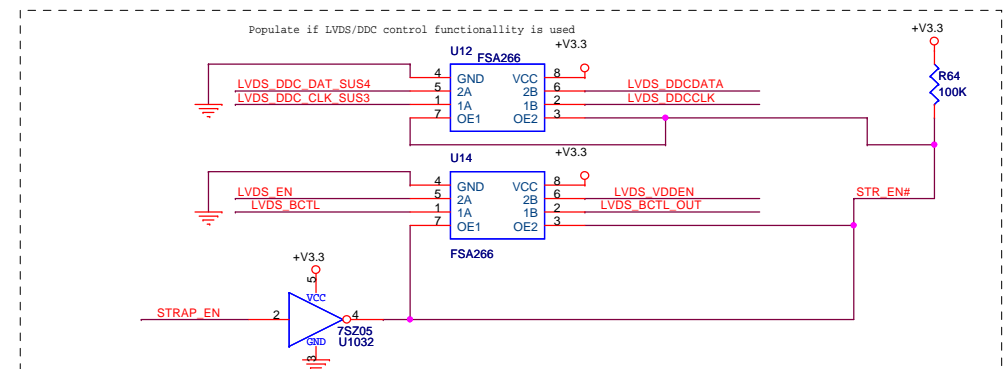


LVDS

LVDS_DN0	2
LVDS_DP0	2
LVDS_DN1	2
LVDS_DP1	2
LVDS_DN2	2
LVDS_DP2	2
LVDS_DN3	2
LVDS_DP3	2
LVDS_CKN	2
LVDS_CKP	2

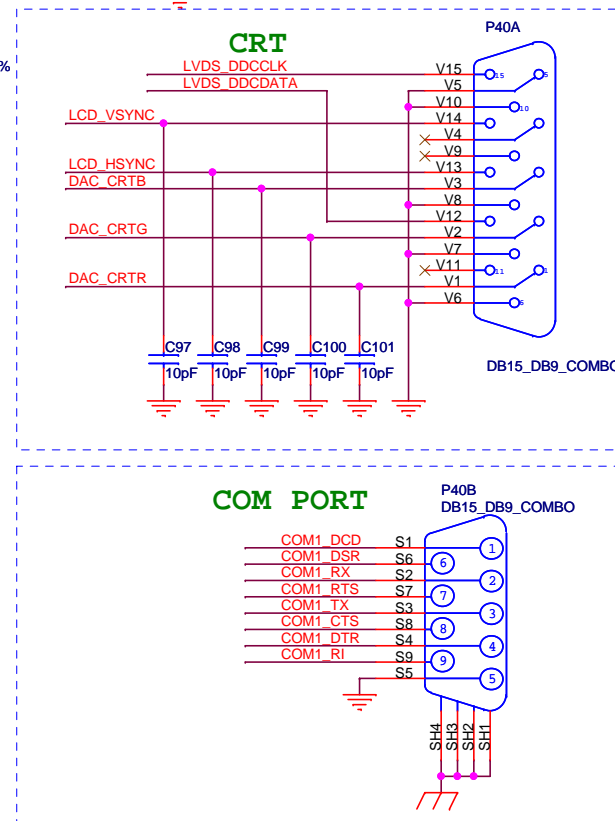
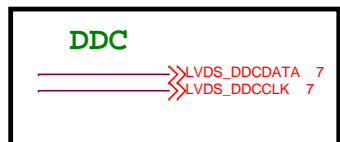
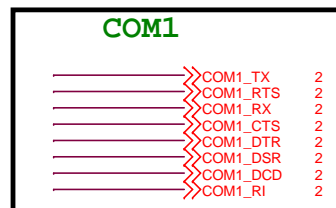
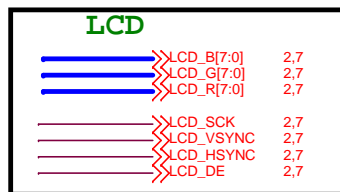
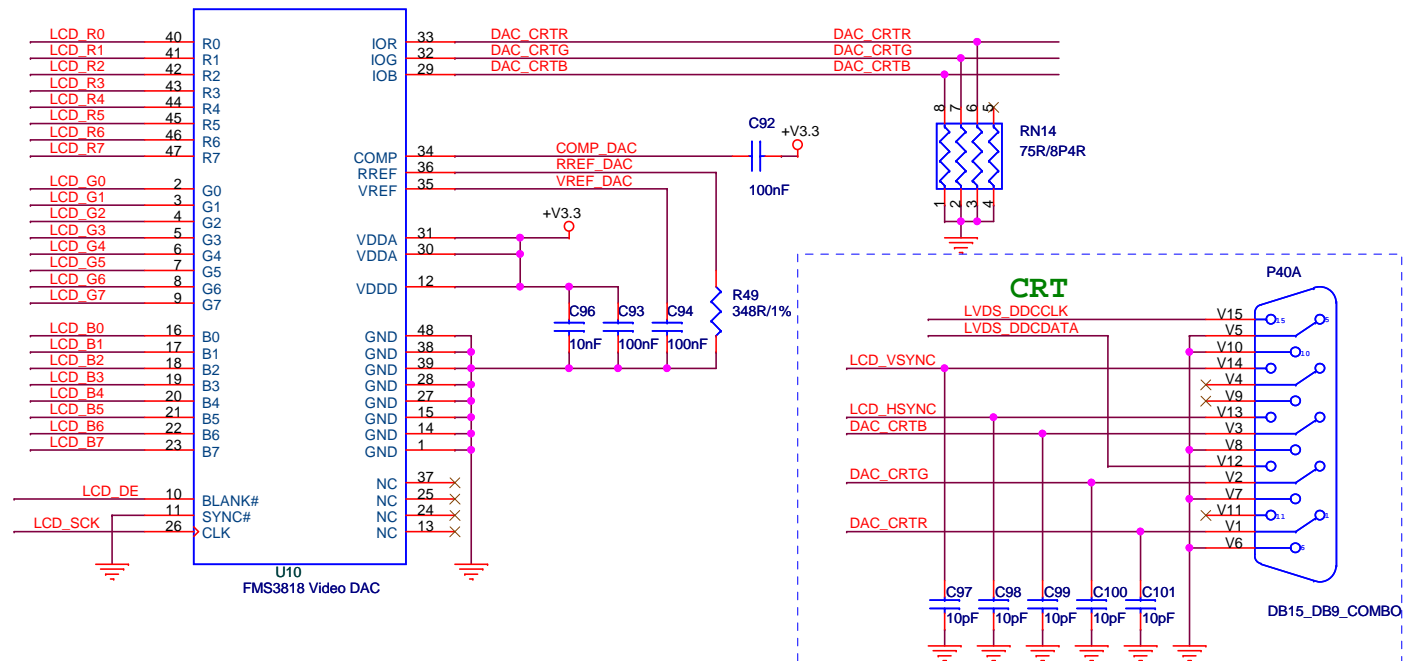
LVDS CONTR

LVDS_BEN	2
LVDS_DDC_DAT_SUS4	2
LVDS_DDC_CLK_SUS3	2
LVDS_BCTL	2
LVDS_BCTL_OUT	14
LVDS_EN	2
LVDS_DDCDATA	8
LVDS_DDCCLK	8

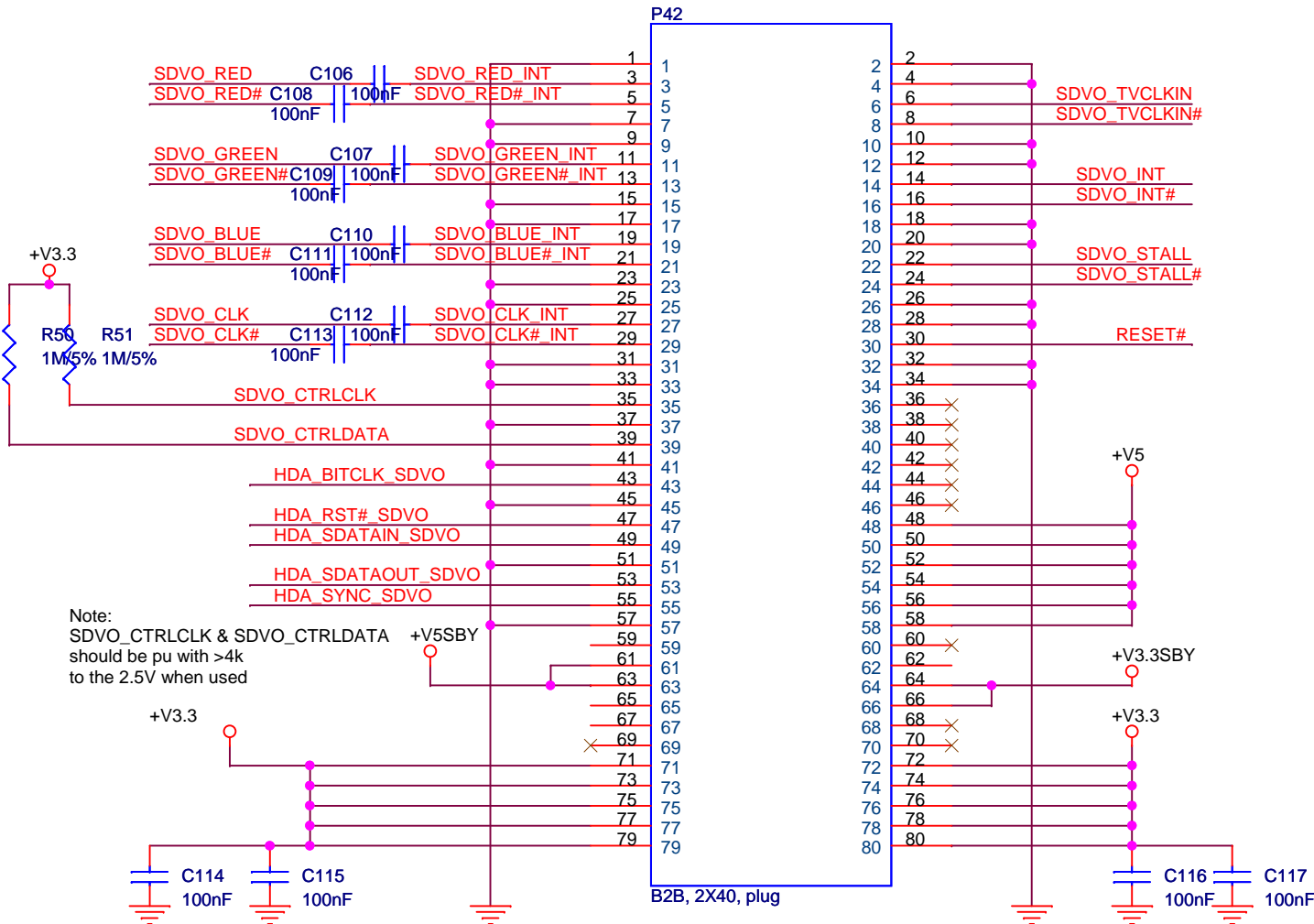


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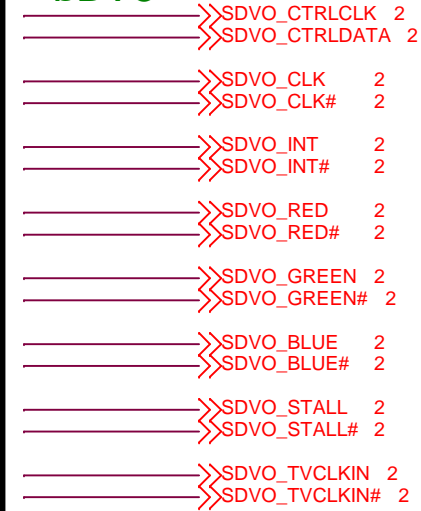
Size	Custom	Title	LCD & LVDS	Rev	1.0
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EXTENSION SLOT



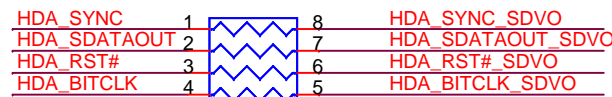
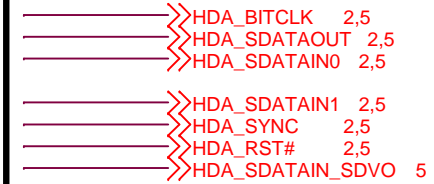
SDVO



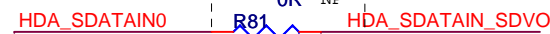
MISC



HDA

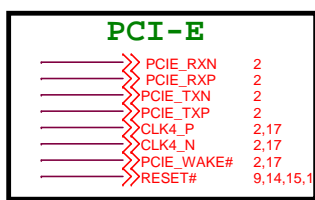
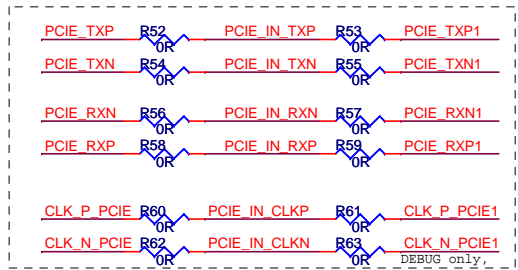
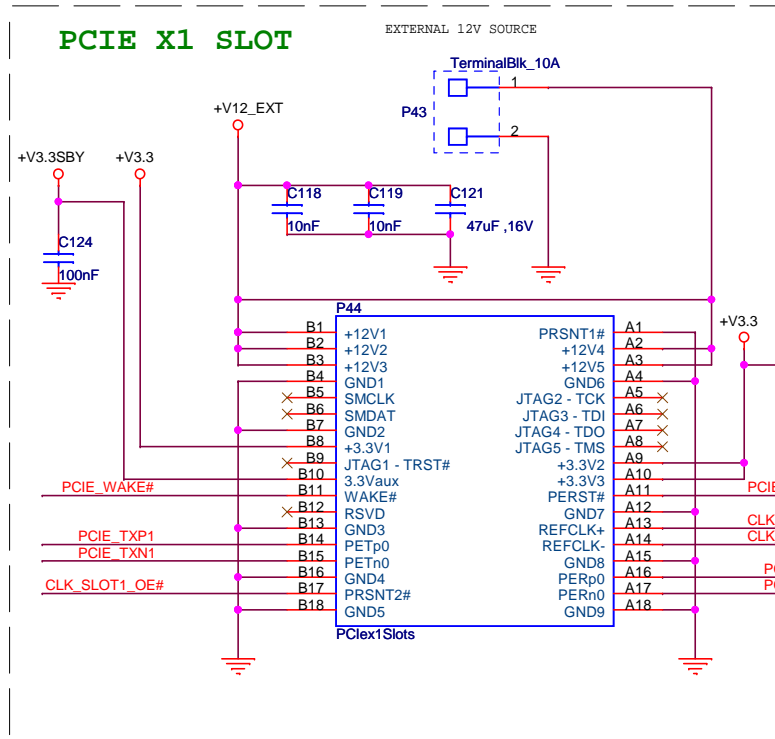
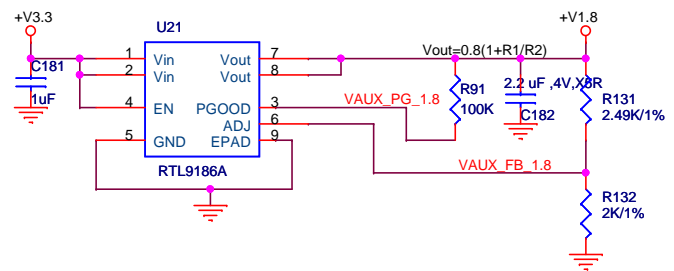
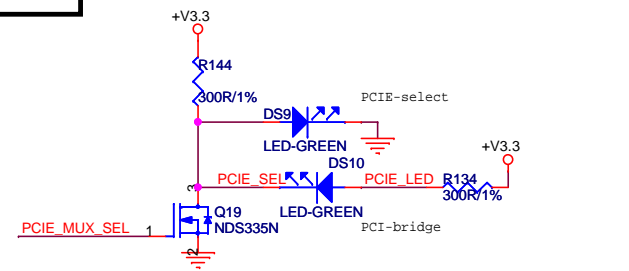
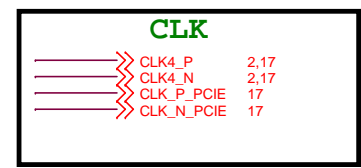
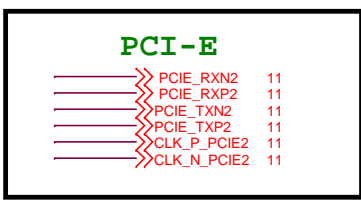
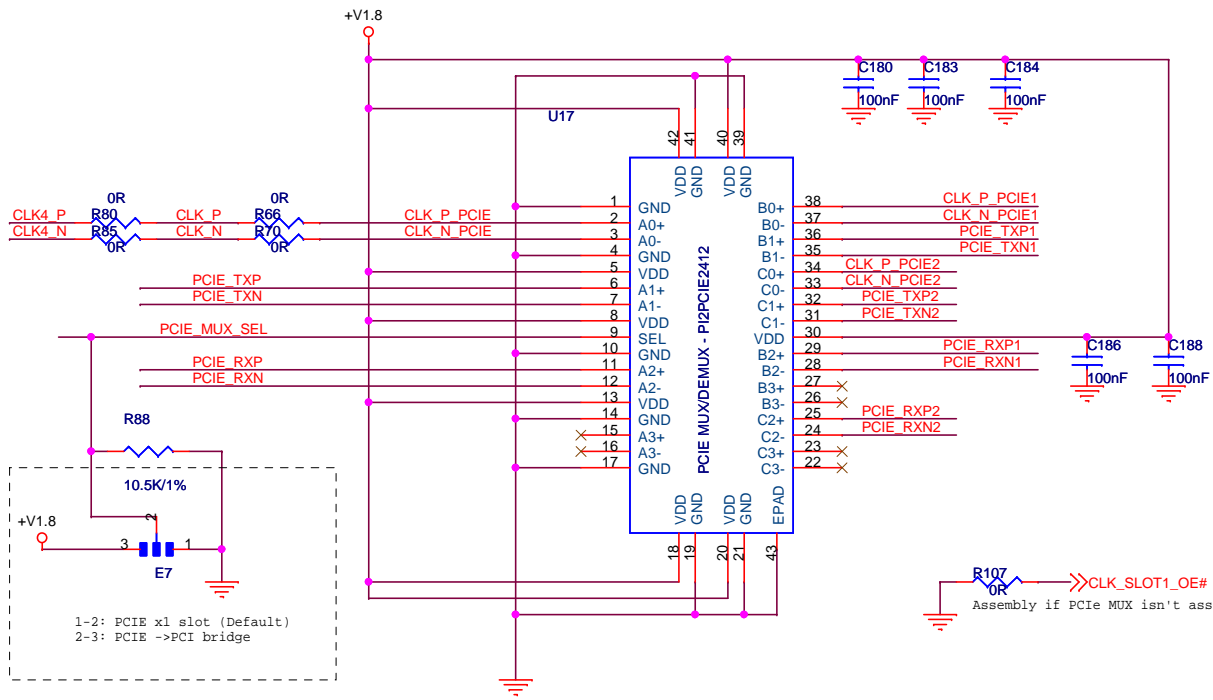


RN22
33R/8P4R



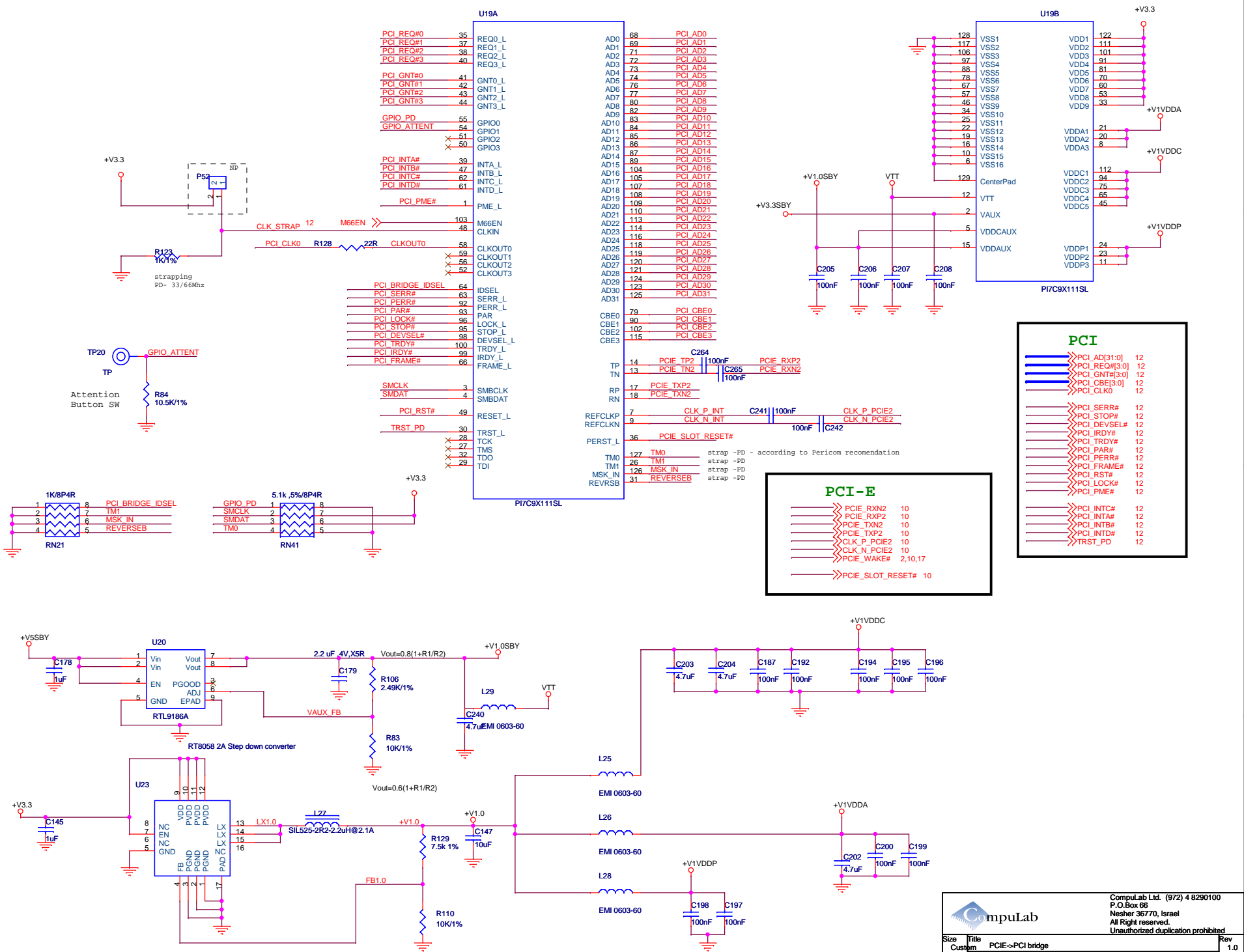
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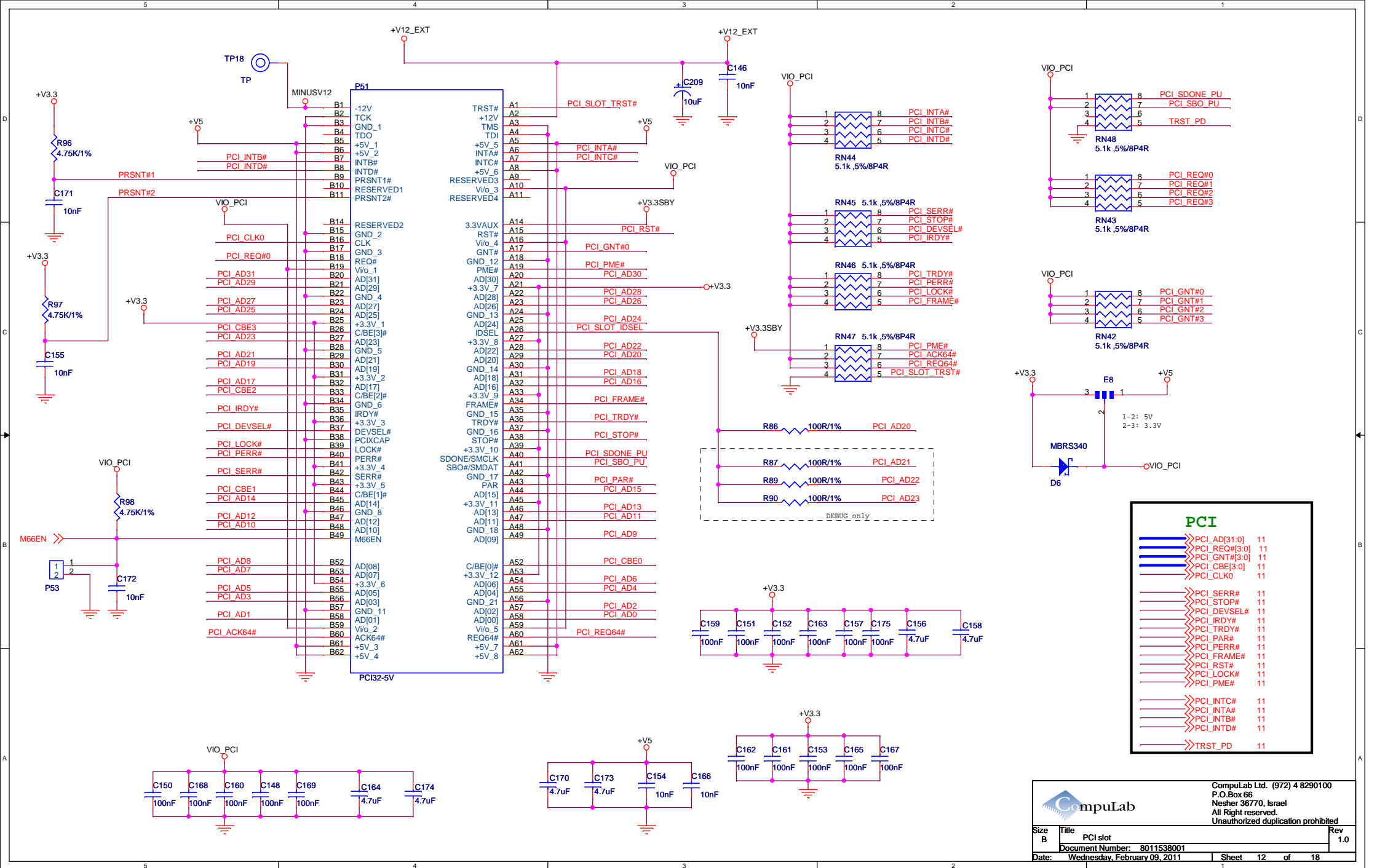
Size A	Title SDVO slot	Document Number: 8011538001	Rev 1.0
Date: Tuesday, February 08, 2011	Sheet 9	of 18	

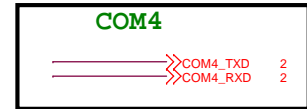
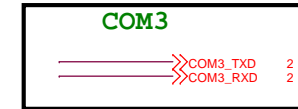
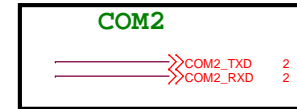
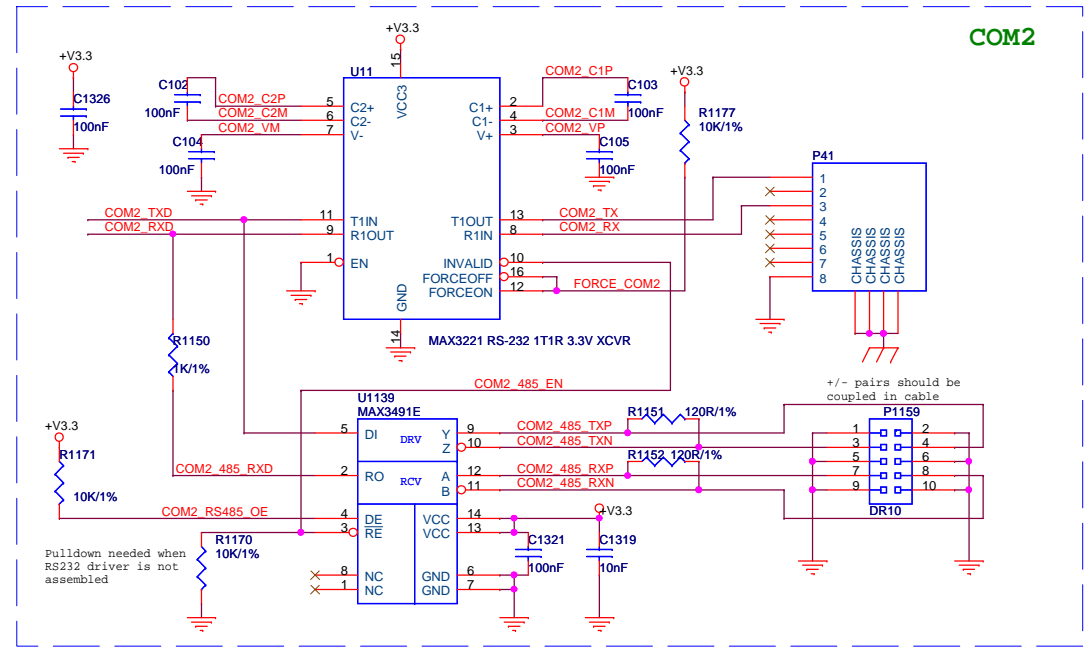
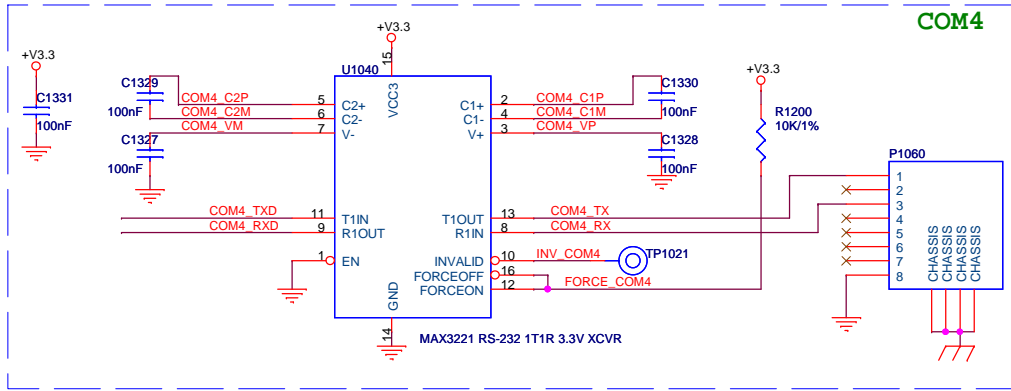
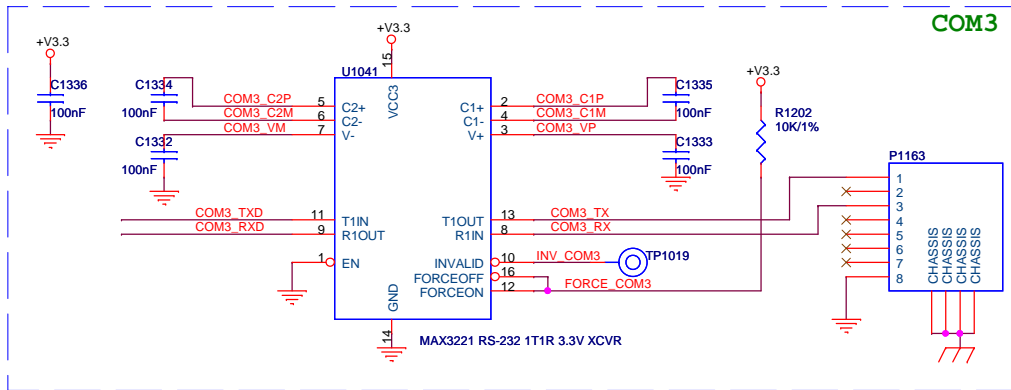


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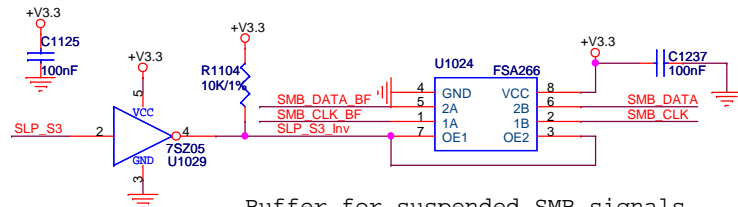
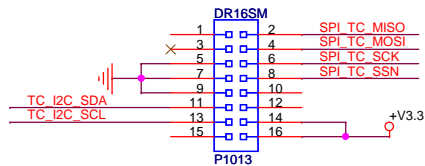
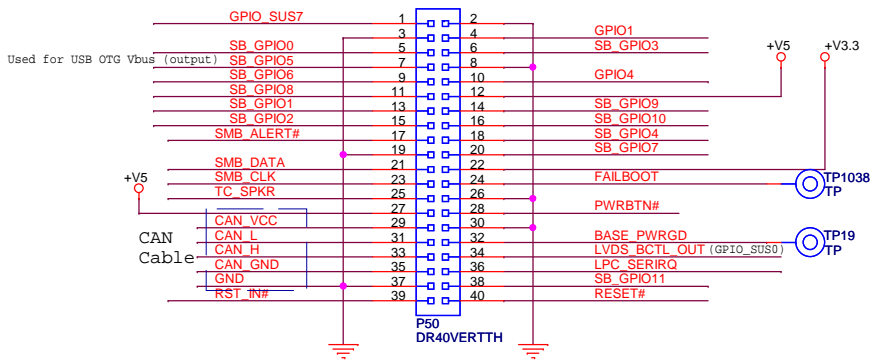
Size Title Rev
Custom PCIE MUX & PCIE slot 1.0
Document Number: 8011538001
Date: Sunday, February 13, 2011 Sheet 10 of 18



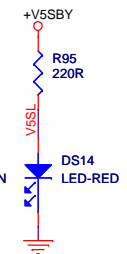
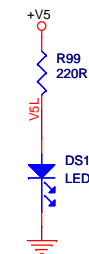
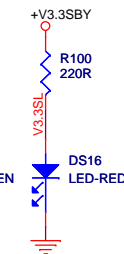
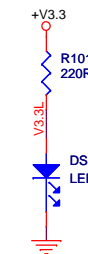
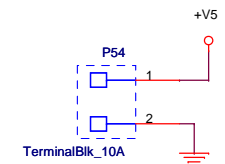
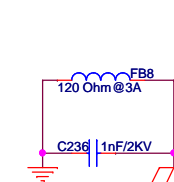
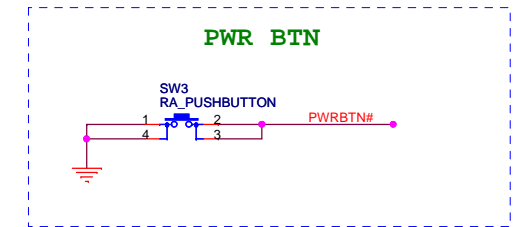
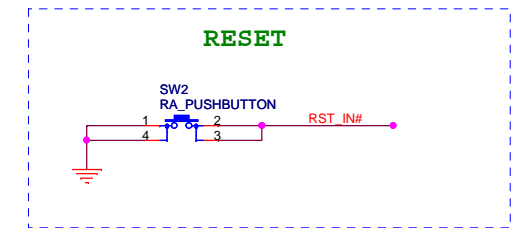
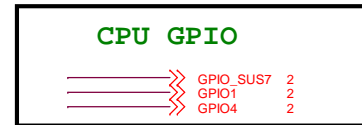
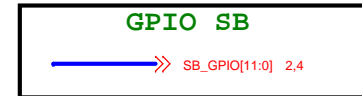
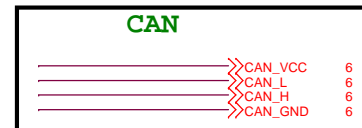
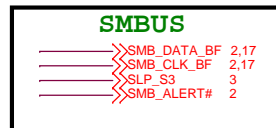
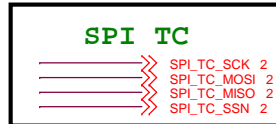
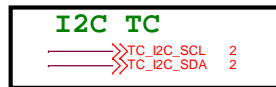
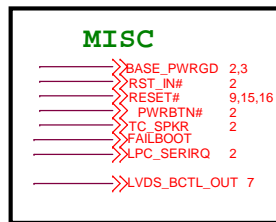
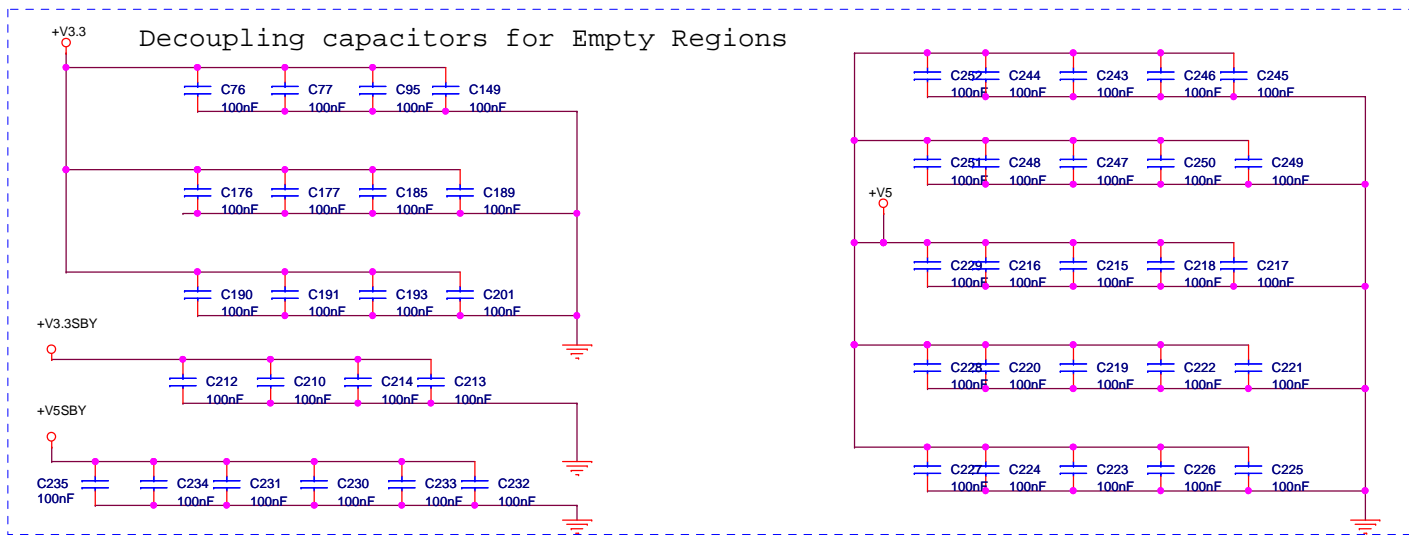




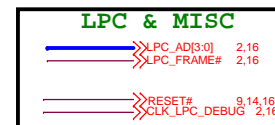
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Size	Title	Rev	
Custom	COM 2.3.4 RS232x3 + 485	1.0	
Document Number: 8011538001			
Date: Thursday, September 08, 2011	Sheet	13	of 18




Buffer for suspended SMB signals



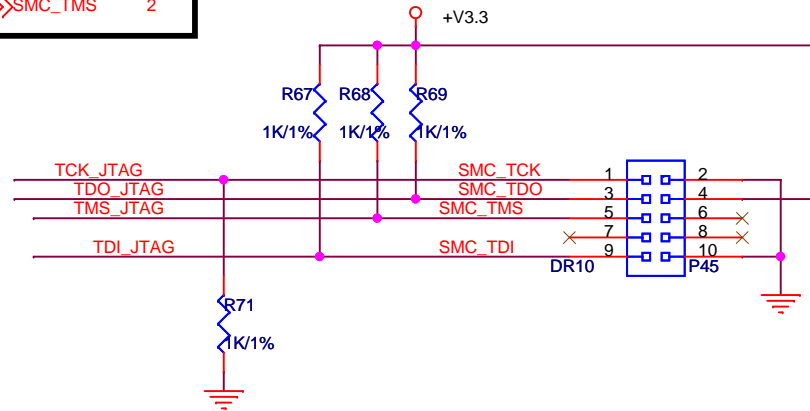
PORT 80 DISPLAY



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Size	Title		Rev
Custom	PORT 80		1.2
Document Number: 8011538001			
Date:	Tuesday, January 25, 2011	Sheet	15 of 18

DEBUG (DON'T COPY THIS PAGE)

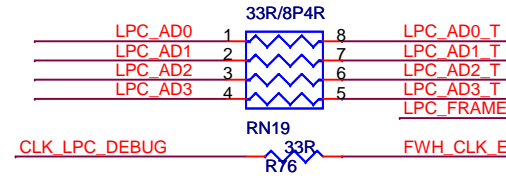
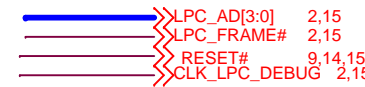
SMC_JTAG



+V5SBY

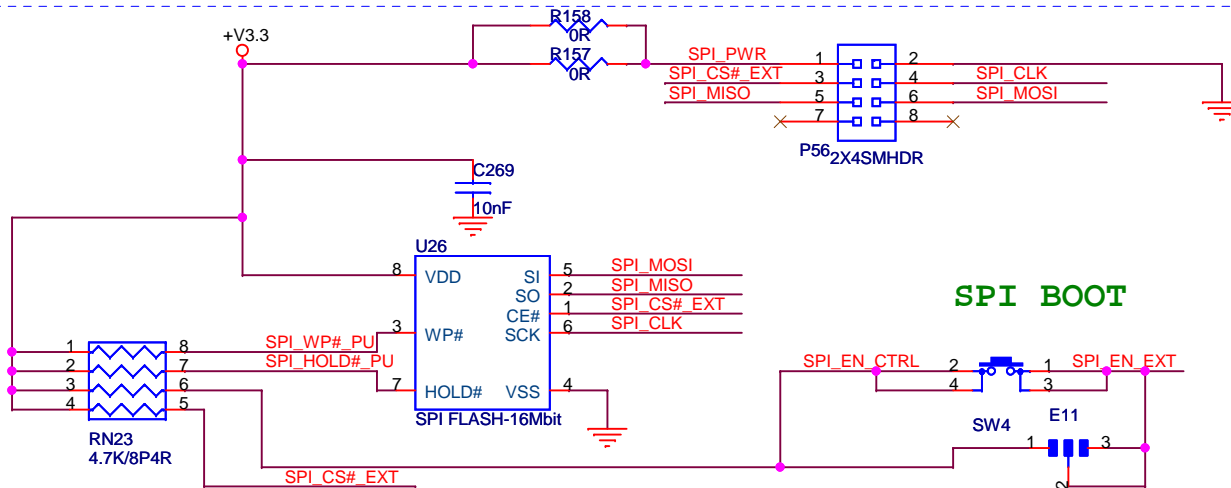
RESET#

+V3.3SBY



FPC_40_05mm

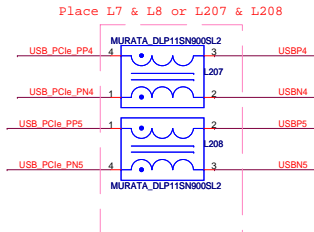
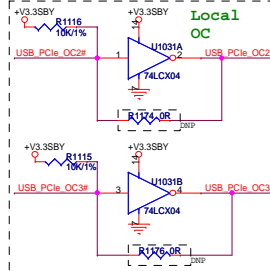
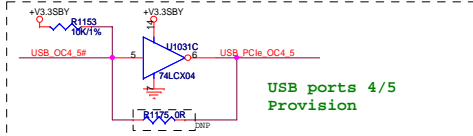
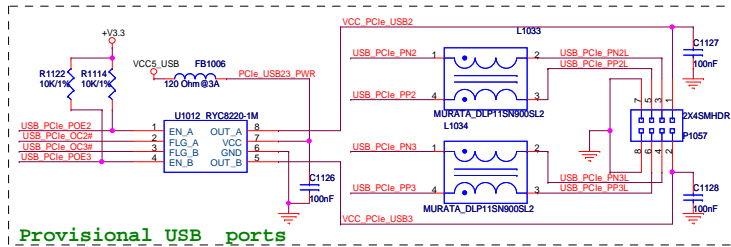
SPI BOOT



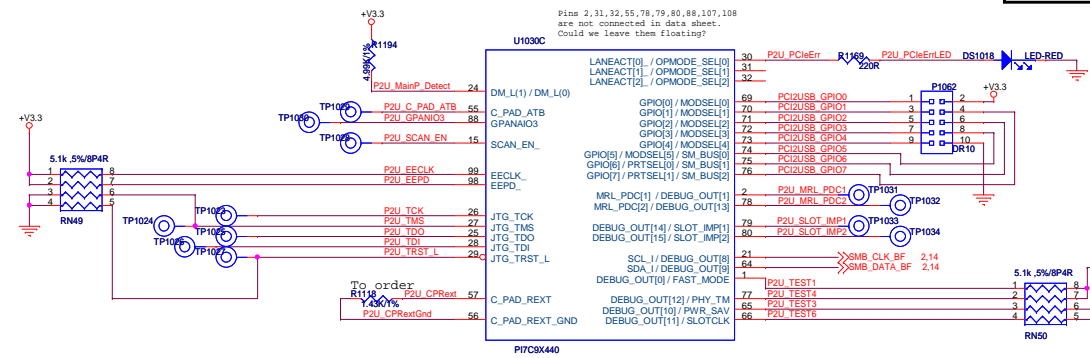
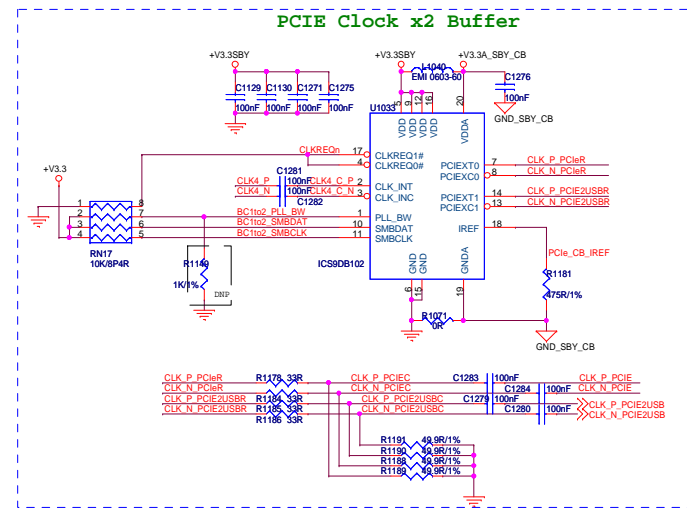
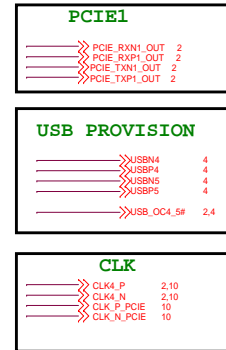
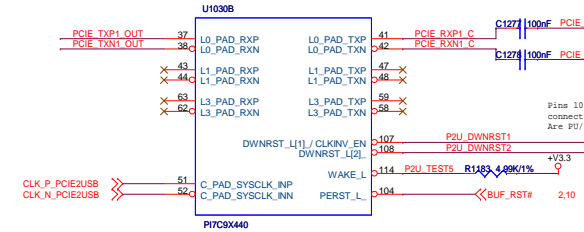
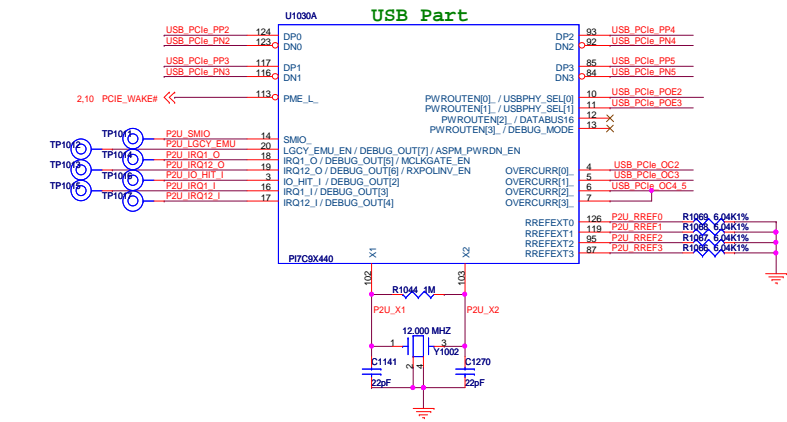
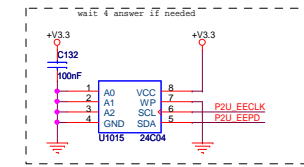
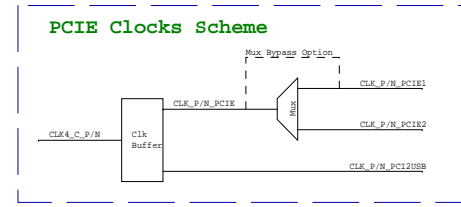
SMC_JTAG



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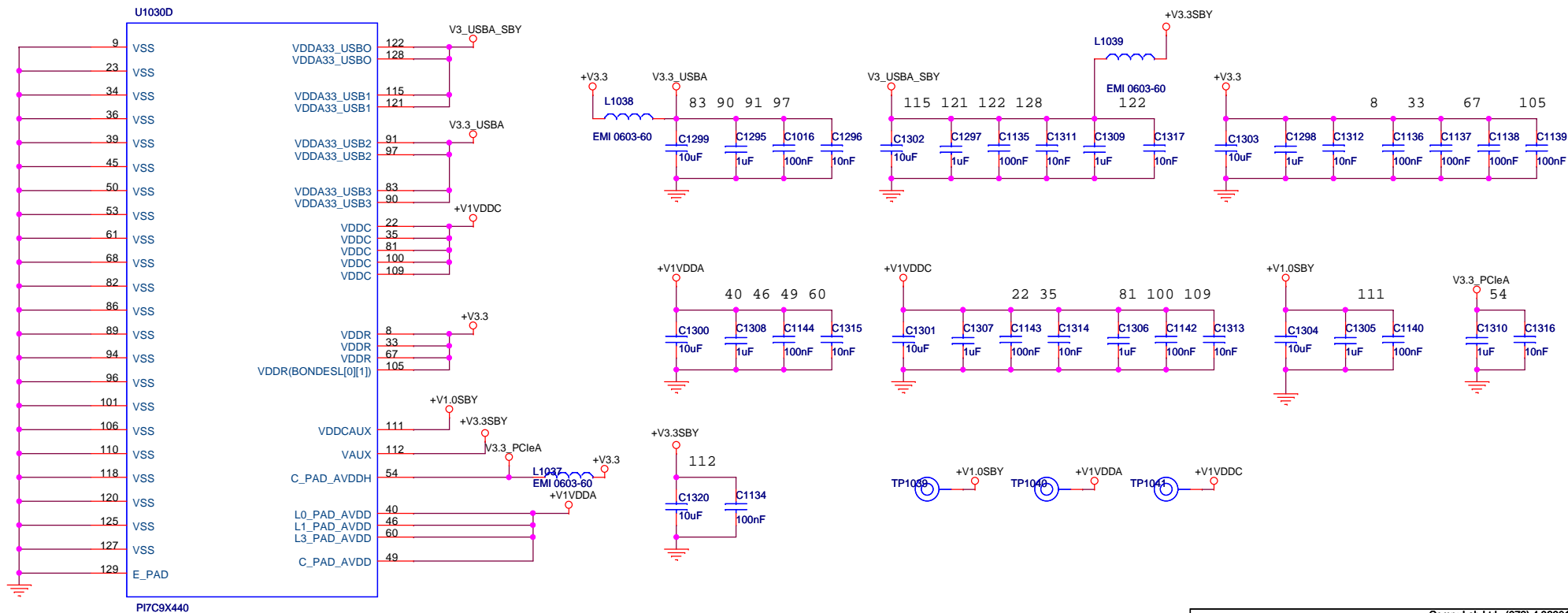



USB Provision for PCIe2USB



Do we need to pullup to regular VCC or Standby (auxiliary) VCC?

DEBUG (DON'T COPY THIS PAGE)





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