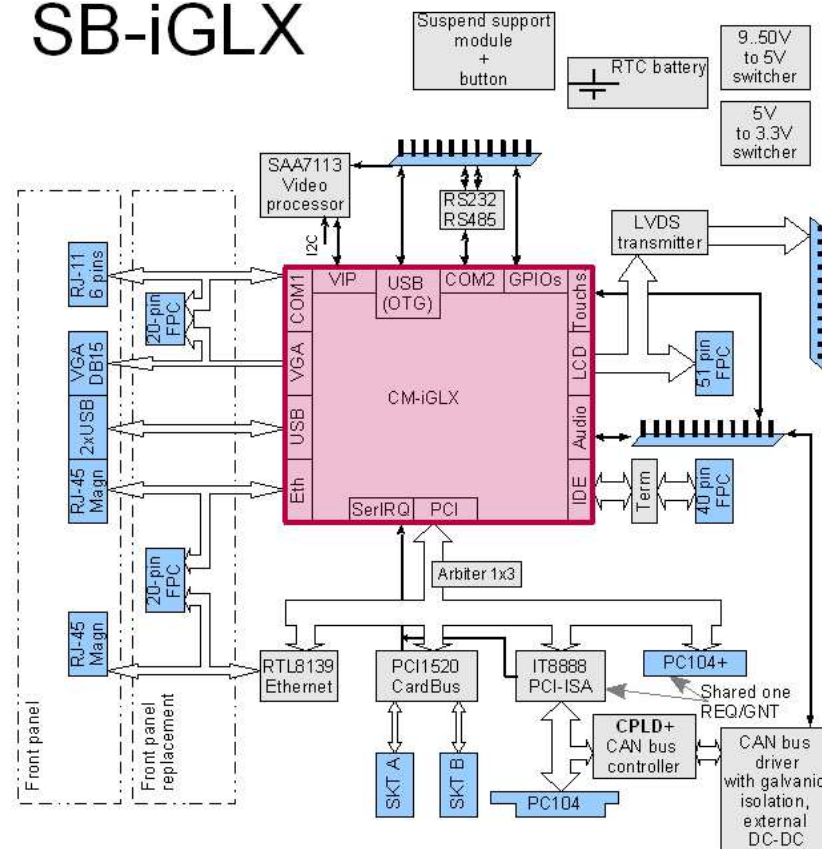


PAGE	FUNCTION
01	INDEX , BLOCK DIAGRAM
02	CAMI CONNECTORS
03	CARDBUS CONTROLLER
04	CARDBUS CONNECTORS
05	PC104+
06	ISA BRIDGE & PC104
07	CAN CONTROLLER
08	ETHERNET
09	FRONT PANEL
10	LCD & LVDS
11	POWER
12	VIDEO INPUT PORT
13	MISC, IDE, SERIAL

DEVICE	IDSEL
Cardbus controller	AD19
Ethernet controller (BASE)	AD24
ISA bridge(IT8888)	AD27

SB-iGLX



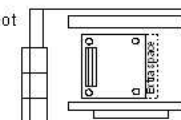
Form Factor – PC104plus + detachable front panel (identical to SB-IVCF)

Front panel placement - identical to SB-IVCF

Standard pinout on all FPC connectors for CompuLab ready adapters

Leave enough space for the module larger than CM-iGLX by 15mm to one side, do not put any part higher than 1.5mm in this area

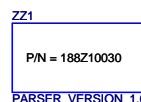
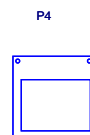
CAN bus driver galvanic isolation – only if there is single-chip DC-DC



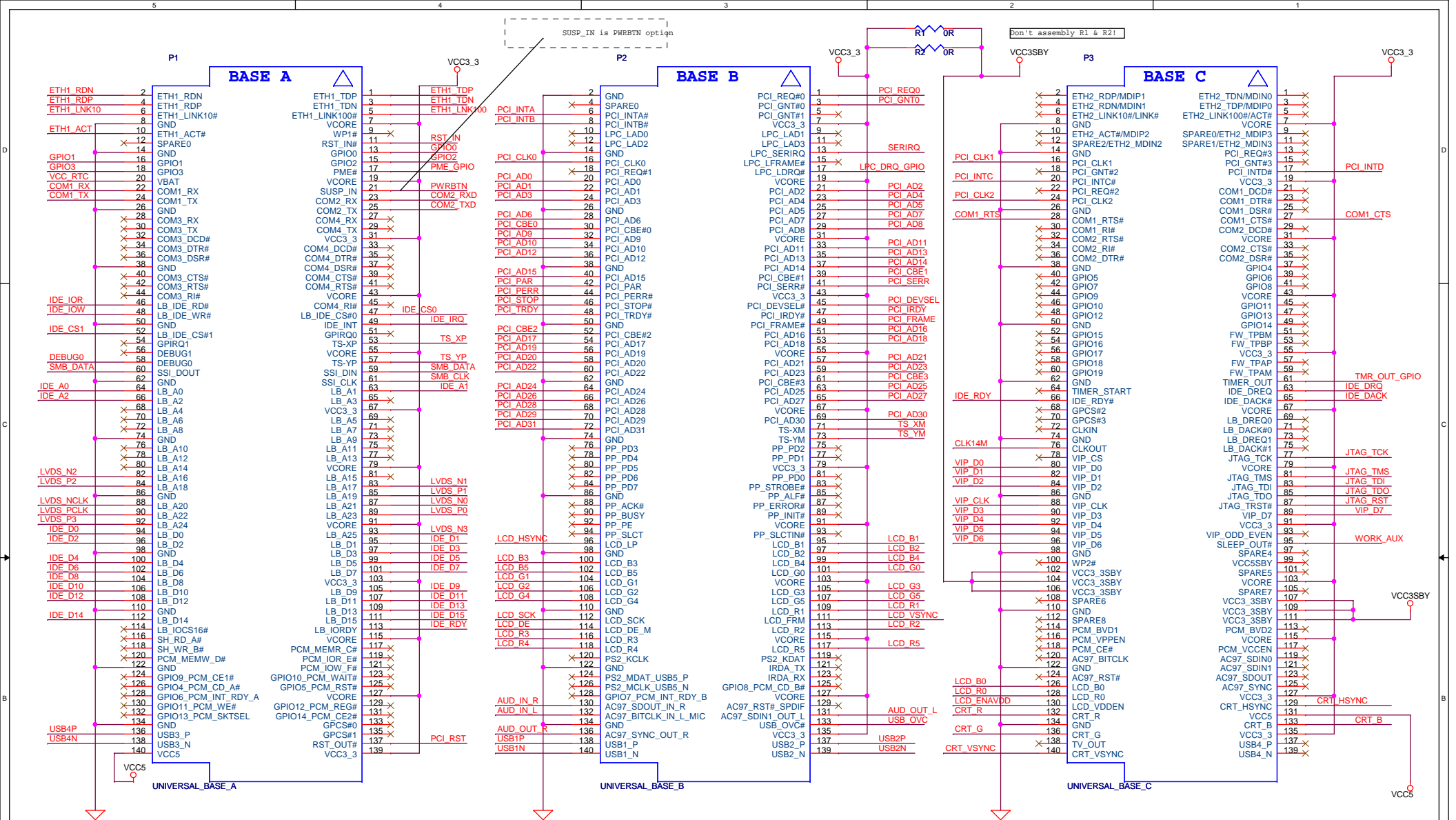
Rev notes:

20-Aug-2006 – LVDS transmitter and additional pins added

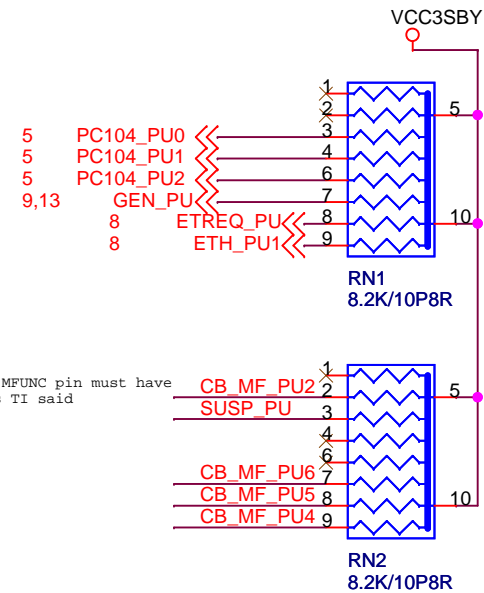
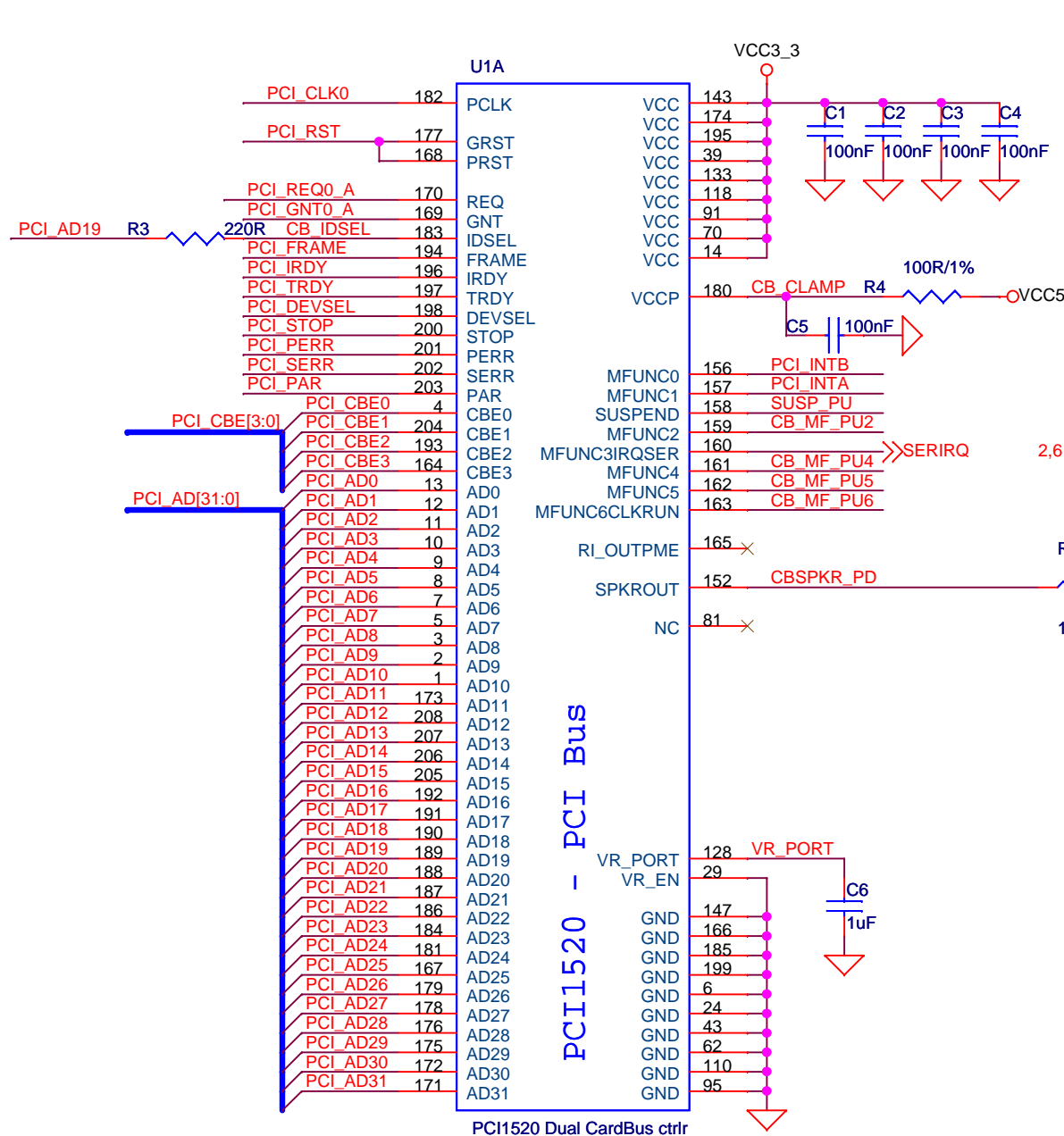
CPLD added to CAN bus controller



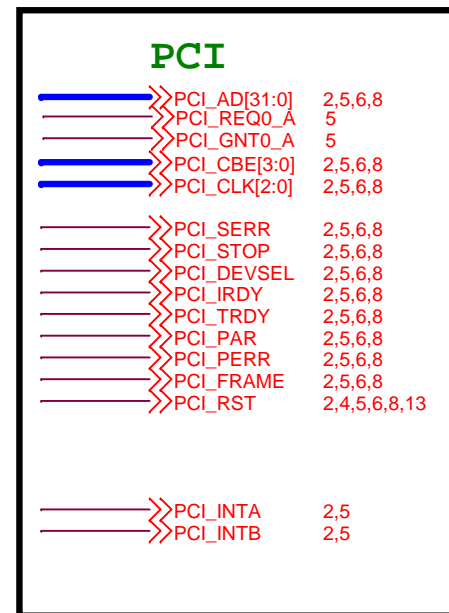
SB-iGLX PCB_1_0




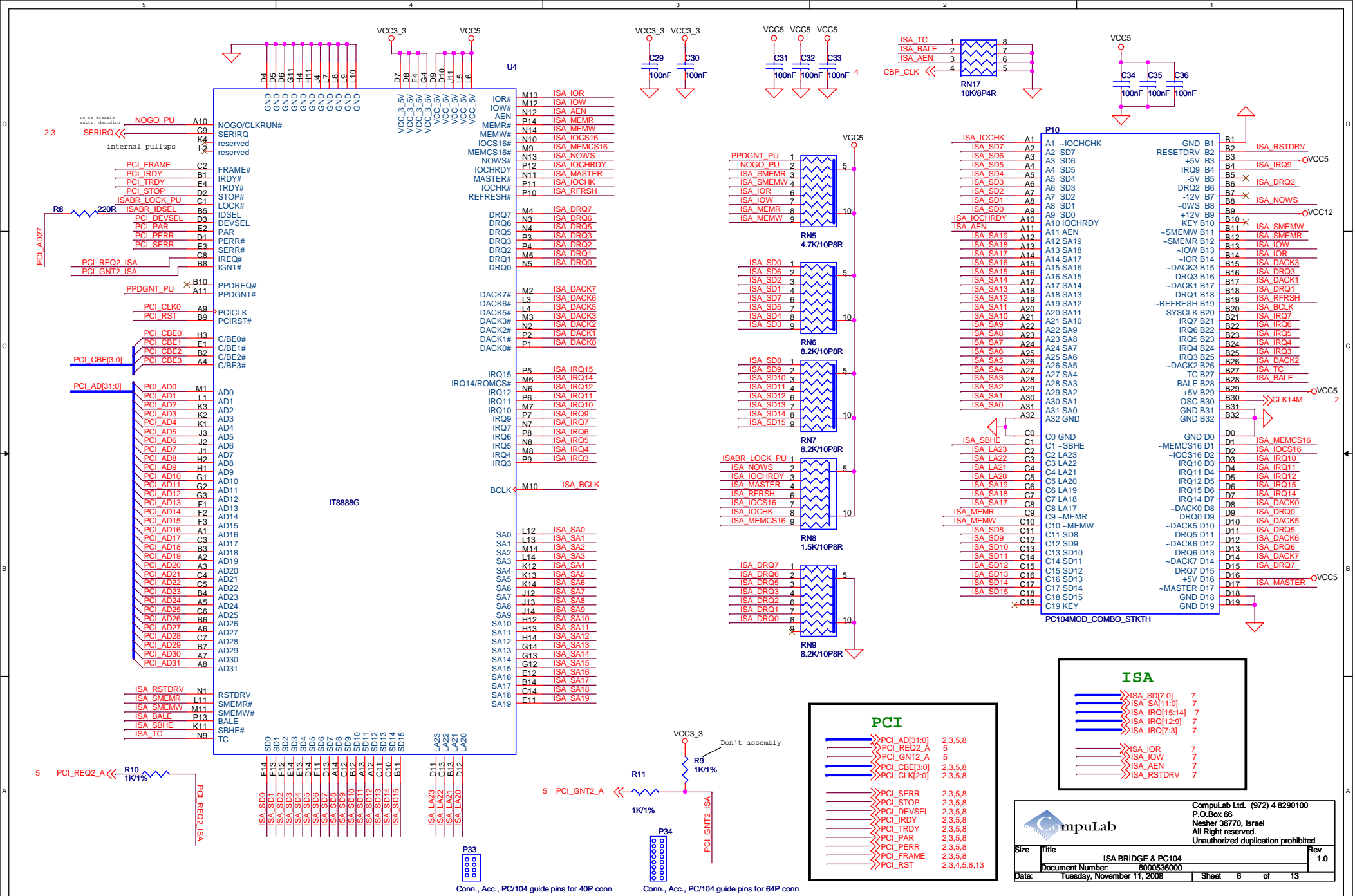
PCI <ul style="list-style-type: none"> PCI_AD[31:0] 3,5,6 PCI_REQ0 5 PCI_GNT0 5 PCI_CBE[3:0] 3,5,6,8 PCI_CLK[2:0] 3,5,6,8 PCI_SERR 3,5,6,8 PCI_STOP 3,5,6,8 PCI_DEVSEL 3,5,6,8 PCI_IRDY 3,5,6,8 PCI_TRDY 3,5,6,8 PCI_PAR 3,5,6,8 PCI_PERR 3,5,6,8 PCI_FRAME 3,5,6,8 PCI_RST 3,4,5,6,8,13 PCI_INTC 5,8 PCI_INTA 3,5 PCI_INTB 3,5 PCI_INTD 5 	IDE <ul style="list-style-type: none"> IDE_D[15:0] 13 IDE_A[2:0] 13 IDE_CS0 13 IDE_CS1 13 IDE_IRQ 13 IDE_IOW 13 IDE_DRQ 13 IDE_DACK 13 IDE_RDY 13 CRT_VSYN 9,13 CRT_HSYN 9,13 CRT_B 9,13 CRT_G 9,13 CRT_R 9,13 	Audio <ul style="list-style-type: none"> AUD_IN_L 13 AUD_IN_R 13 AUD_OUT_L 13 AUD_OUT_R 13 	Touch Screen <ul style="list-style-type: none"> TS_XM 13 TS_YM 13 TS_XP 13 TS_YP 13 	USB <ul style="list-style-type: none"> USB1N 9 USB1P 9 USB2N 9 USB4N 13 USB4P 13 USB_OVC 9,13 	Video Input Port <ul style="list-style-type: none"> VIP_D[7:0] 12 VIP_CLK 12 	LCD <ul style="list-style-type: none"> LCD_ENAVDD 10 LCD_VSYN 10 LCD_HSYN 10 LCD_SCK 10 LCD_DE 10 LCD_R[5:0] 10 LCD_G[5:0] 10 LCD_R[5:0] 10 	Ethernet <ul style="list-style-type: none"> ETH1_RDP 8,9 ETH1_RDN 8,9 ETH1_TDP 8,9 ETH1_TDN 8,9 ETH1_LNK10 8,9 ETH1_LNK100 8,9 ETH1_ACT 8,9 	GPIO <ul style="list-style-type: none"> GPIO[3:0] 12 	COM1 <ul style="list-style-type: none"> COM1_RX 9,13 COM1_TX 9,13 COM1_RTS 9,13 COM1_CTS 9,13 	COM2 <ul style="list-style-type: none"> COM2_RXD 13 COM2_TXD 13 	MISC <ul style="list-style-type: none"> TMR_OUT_GPIO 13 RST_IN 13 DEBUG0 13 VCC_RTC 13 CLK14M 6 PCI_DRQ_GPIO 13 SERIRQ 3,6 WORK_AUX 11,12 PWRBTN 13 PME_GPIO 13 	JTAG <ul style="list-style-type: none"> JTAG_TMS 13 JTAG_TCK 13 JTAG_RST 13 JTAG_TDI 13 JTAG_TDO 13 	SMBUS <ul style="list-style-type: none"> SMB_DATA 12,13 SMB_CLK 12,13 	LVDS <ul style="list-style-type: none"> LVDS_N[3:0] 10,12 LVDS_P[3:0] 10,12 LVDS_NCLK 10,12 LVDS_PCLK 10,12
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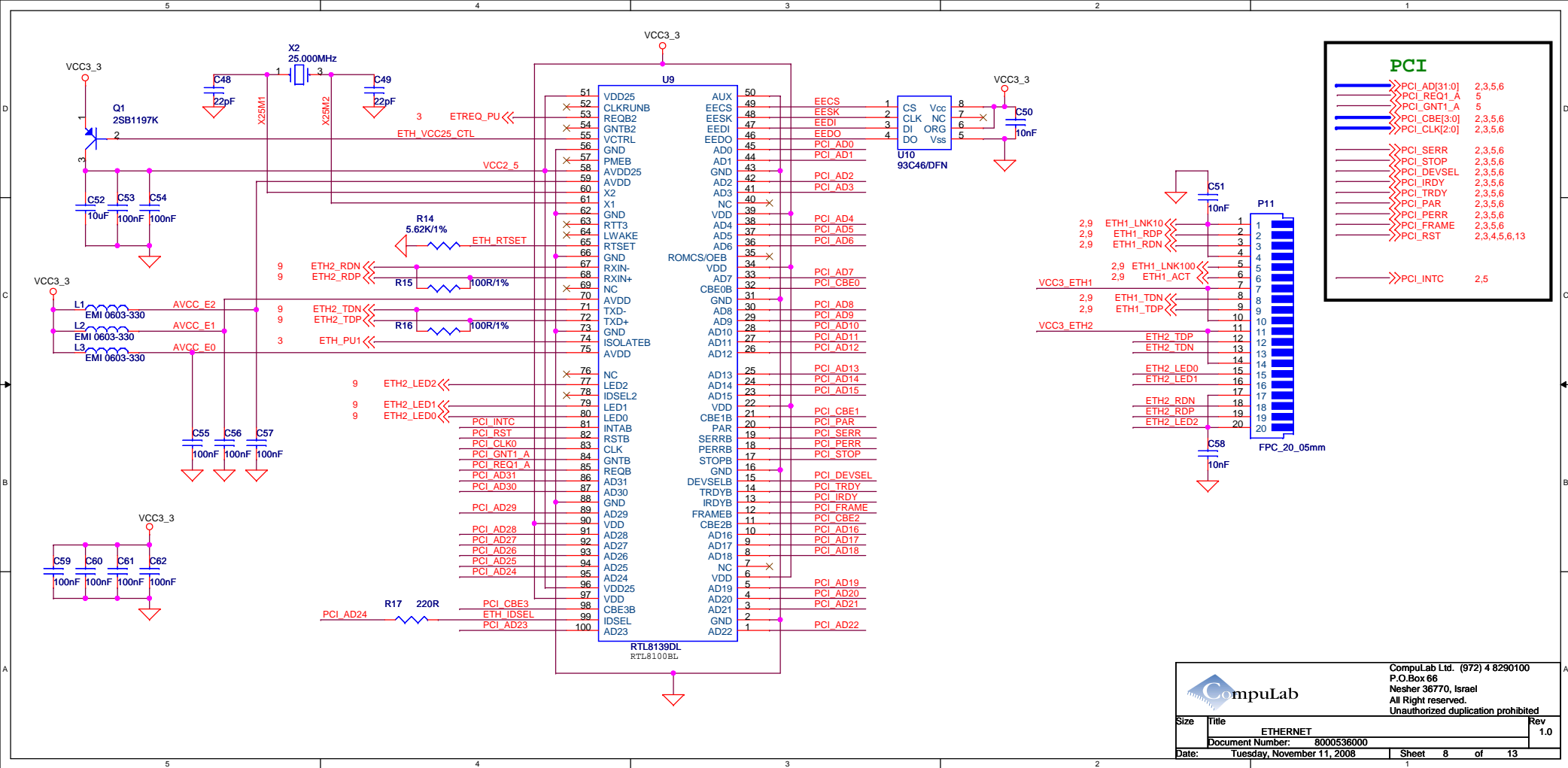


Each MFUNC pin must have
PU as TI said

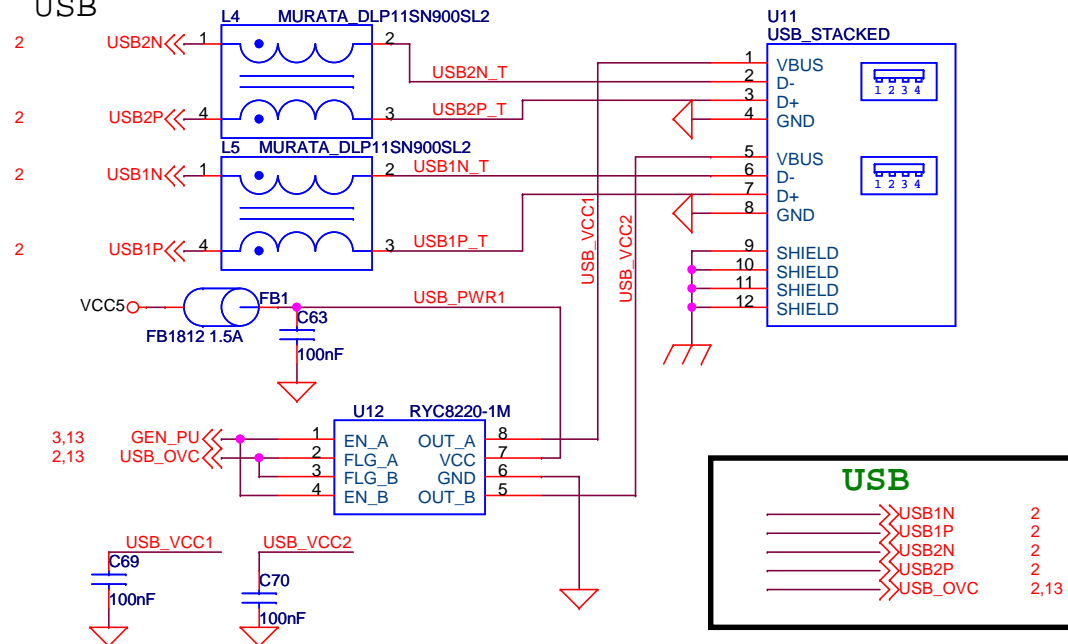


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Size A	Title CARDBUS CONTROLLER		Rev 1.0
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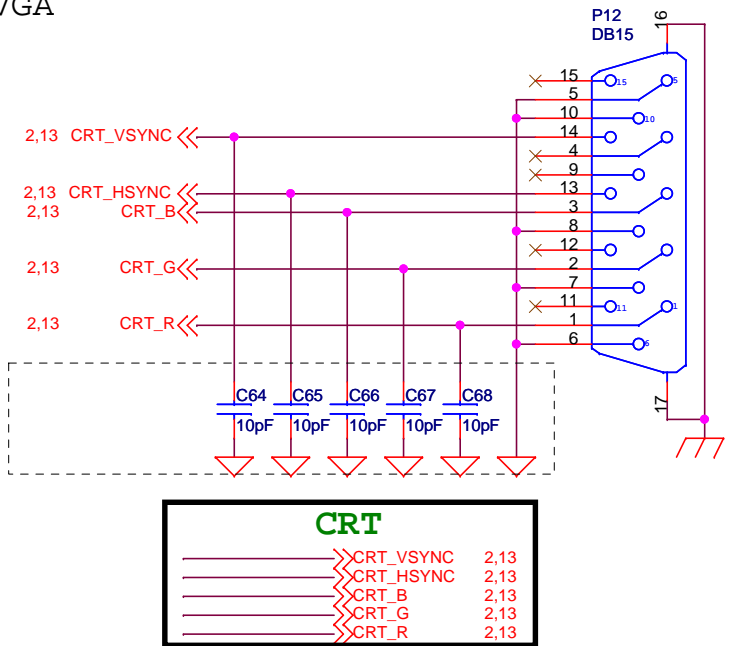




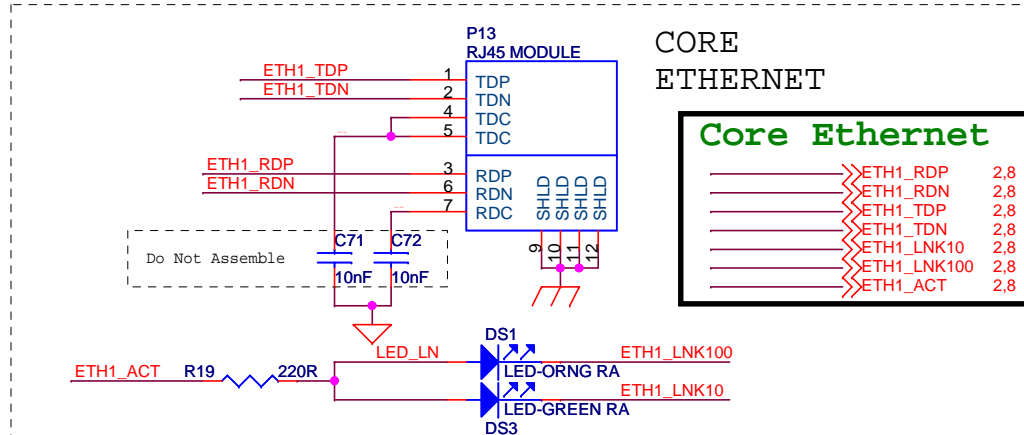
USB



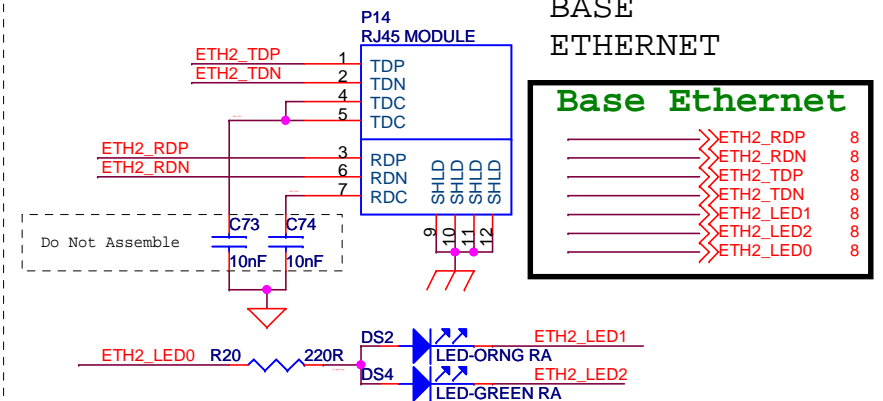
VGA



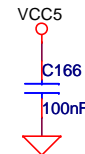
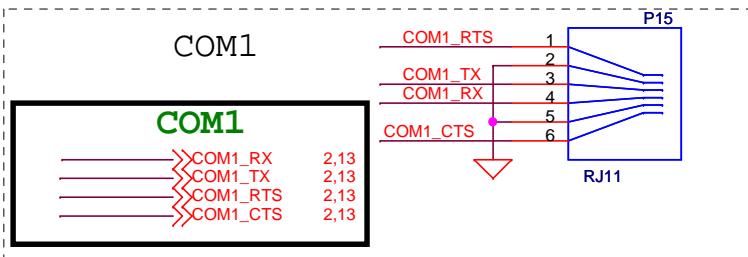
CORE ETHERNET



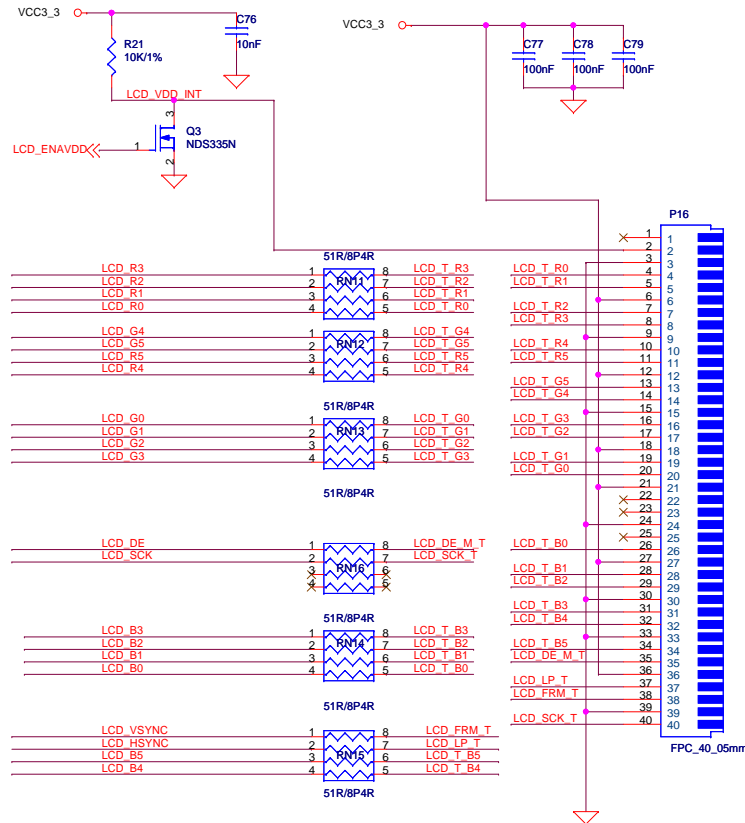
BASE ETHERNET



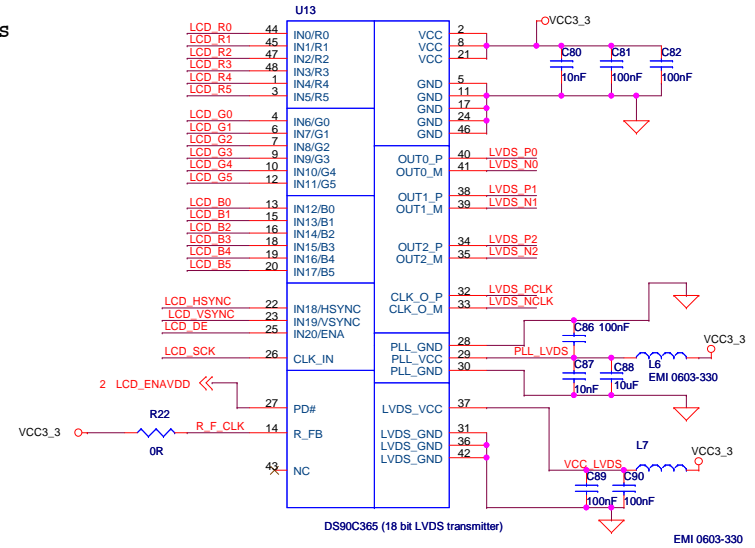
COM1



LCD			
>>>	LCD_ENAVDD	2	
>>>	LCD_VSYNC	2	
>>>	LCD_HSYNC	2	
>>>	LCD_SCK	2	
>>>	LCD_DE	2	
>>>	LCD_B[5:0]	2	
>>>	LCD_G[5:0]	2	
>>>	LCD_R[5:0]	2	



LVDS



LVDS			
>>>	LVDS_N[3:0]	2	12
>>>	LVDS_P[3:0]	2	12
>>>	LVDS_NCLK	2	12
>>>	LVDS_PCLK	2	12

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	LCD & LVDS	8000536000	1.0
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